

## Medium Voltage Thyristor

### Types K0349LC600 to K0349LC650

Old Type No.: P201CH60-65

#### Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V <sub>DRM</sub>	Repetitive peak off-state voltage, (note 1)	6000-6500	V
V <sub>DSM</sub>	Non-repetitive peak off-state voltage, (note 1)	6000-6500	V
V <sub>RRM</sub>	Repetitive peak reverse voltage, (note 1)	6000-6500	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage, (note 1)	6100-6600	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I <sub>T(AV)M</sub>	Maximum average on-state current, T <sub>sink</sub> =55°C, (note 2)	349	A
I <sub>T(AV)M</sub>	Maximum average on-state current. T <sub>sink</sub> =85°C, (note 2)	221	A
I <sub>T(AV)M</sub>	Maximum average on-state current. T <sub>sink</sub> =85°C, (note 3)	134	A
I <sub>T(RMS)</sub>	Nominal RMS on-state current, T <sub>sink</sub> =25°C, (note 2)	705	A
I <sub>T(d.c.)</sub>	D.C. on-state current, T <sub>sink</sub> =25°C, (note 4)	622	A
I <sub>TSM</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>RM</sub> =0.6V <sub>RRM</sub> , (note 5)	4800	kA
I <sub>TSM2</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>RM</sub> ≤10V, (note 5)	5300	kA
I <sup>2</sup> t	I <sup>2</sup> t capacity for fusing t <sub>p</sub> =10ms, V <sub>RM</sub> =0.6V <sub>RRM</sub> , (note 5)	115×10 <sup>3</sup>	A <sup>2</sup> s
I <sup>2</sup> t	I <sup>2</sup> t capacity for fusing t <sub>p</sub> =10ms, V <sub>RM</sub> ≤10V, (note 5)	140×10 <sup>3</sup>	A <sup>2</sup> s
(di/dt) <sub>cr</sub>	Critical rate of rise of on-state current (repetitive), (Note 6)	150	A/μs
	Critical rate of rise of on-state current (non-repetitive), (Note 6)	300	A/μs
V <sub>RGM</sub>	Peak reverse gate voltage	5	V
P <sub>G(AV)</sub>	Mean forward gate power	2	W
P <sub>GM</sub>	Peak forward gate power	30	W
V <sub>GD</sub>	Non-trigger gate voltage, (Note 7)	0.25	V
T <sub>j op</sub>	Operating temperature range	-40 to +115	°C
T <sub>stg</sub>	Storage temperature range	-40 to +150	°C

Notes: -

- 1) De-rating factor of 0.13% per °C is applicable for T<sub>j</sub> below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 115°C T<sub>j</sub> initial.
- 6) V<sub>D</sub>=67% V<sub>DRM</sub>, I<sub>FG</sub>=2A, t<sub>r</sub>≤0.5μs, T<sub>case</sub>=115°C.
- 7) Rated V<sub>DRM</sub>.

**Characteristics**

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
$V_{TM}$	Maximum peak on-state voltage	-	-	2.8	$I_{TM}=500A$	V
$V_{T0}$	Threshold voltage	-	-	1.568		V
$r_T$	Slope resistance	-	-	2.428		m $\Omega$
$(dv/dt)_{cr}$	Critical rate of rise of off-state voltage	1000	-	-	$V_D=80\% V_{DRM}$ , linear ramp, Gate O/C	V/ $\mu$ s
$I_{DRM}$	Peak off-state current	-	-	50	Rated $V_{DRM}$	mA
$I_{RRM}$	Peak reverse current	-	-	50	Rated $V_{RRM}$	mA
$V_{tr}$	On-state recovery voltage	-	9.0	-	$I_T=3 \times I_{T(AV)M}$ , $t_p=10ms$ , $T_{case}=25^\circ C$	V
$V_{GT}$	Gate trigger voltage	-	-	3.0	$T_j=25^\circ C$ , $V_D=10V$ , $I_T=3A$	V
$I_{GT}$	Gate trigger current	-	-	300		mA
$V_{GD}$	Gate non-trigger voltage	-	-	0.25	Rated $V_{DRM}$	V
$I_H$	Holding current	-	-	1000	$T_j=25^\circ C$	mA
$t_{gd}$	Gate controlled turn-on delay time	-	0.6	1.2	$I_{FG}=2A$ , $t_r=0.5\mu s$ , $V_D=67\% V_{DRM}$ , $I_{TM}=1000A$ , $di/dt=10A/\mu s$ , $T_j=25^\circ C$	$\mu$ s
$t_{gt}$	Turn-on time	-	6.0	9.0		
$Q_{rr}$	Recovered Charge	-	3100	-		$\mu$ C
$Q_{ra}$	Recovered Charge, 50% chord	-	2100	2500	$I_{TM}=1000A$ , $t_p=1ms$ , $di/dt=10A/\mu s$ , $V_r=50V$	$\mu$ C
$I_{rm}$	Reverse recovery current	-	130	-		A
$t_{rr}$	Reverse recovery time, 50% chord	-	30	-		$\mu$ s
$t_q$	Turn-off time	-	800	-	$I_{TM}=1000A$ , $t_p=1ms$ , $di/dt=10A/\mu s$ , $V_r=50V$ , $V_{dr}=33\% V_{DRM}$ , $dV_{dr}/dt=20V/\mu s$	$\mu$ s
		-	1100	-	$I_{TM}=1000A$ , $t_p=1ms$ , $di/dt=10A/\mu s$ , $V_r=50V$ , $V_{dr}=33\% V_{DRM}$ , $dV_{dr}/dt=200V/\mu s$	
$R_{thJK}$	Thermal resistance, junction to heatsink	-	-	0.047	Double side cooled	K/W
		-	-	0.094	Single side cooled	K/W
F	Mounting force	10	-	20		kN
$W_t$	Weight	-	340	-		g

Notes: -

1) Unless otherwise indicated  $T_j=115^\circ C$ .

## Notes on Ratings and Characteristics

### 1.0 Voltage Grade Table

Voltage Grade	$V_{DRM}$ $V_{DSM}$ $V_{RRM}$ V	$V_{RSM}$ V	$V_D$ $V_R$ DC V
60	6000	6100	3000
62	6200	6300	3100
64	6400	6500	3200
65	6500	6600	3250

### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

### 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for  $T_j$  below 25°C.

### 4.0 Repetitive dv/dt

Standard dv/dt is 1000V/μs.

### 5.0 Frequency Ratings

The curves illustrated in figures 17 & 18 are for guidance only and are superseded by the maximum ratings shown on page 1. For operation above line frequency, please consult the factory for assistance.

### 6.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

### 7.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 400A/μs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 200A/μs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

### 8.0 Square wave frequency ratings

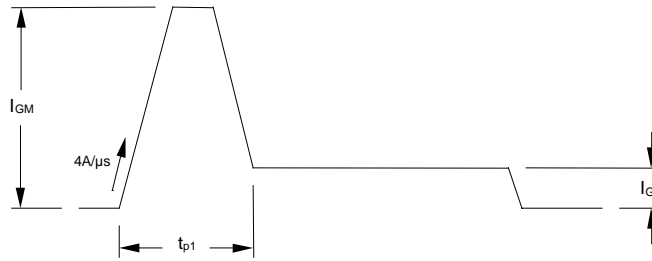
These ratings are given for load component rate of rise of on-state current of 50A/μs.

### 9.0 Duty cycle lines

The 100% duty cycle is represented on the frequency ratings by a straight line. Other duties can be included as parallel to the first.

10.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of  $I_{GM}$  should be between five and ten times  $I_{GT}$ , which is shown on page 2. Its duration ( $t_{p1}$ ) should be  $20\mu s$  or sufficient to allow the anode current to reach ten times  $I_L$ , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current  $I_G$  should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times  $I_{GT}$ .

11.0 Computer Modelling Parameters

11.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff \cdot r_T \cdot W_{AV}}}{2 \cdot ff \cdot r_T} \text{ and: } W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{jmax} - T_{Hs}$$

Where  $V_{T0}=1.568V$ ,  $r_T=2.428m\Omega$ ,

$R_{th}$  = Supplementary thermal impedance, see table below and

$ff$  = Form factor, see table below.

Supplementary Thermal Impedance							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave Double Side Cooled	0.0509	0.0504	0.0499	0.0494	0.0485	0.0474	0.047
Square wave Single Side Cooled	0.0987	0.0981	0.0976	0.0972	0.0963	0.0952	0.094
Sine wave Double Side Cooled	0.0505	0.0498	0.0493	0.0489	0.0475		
Sine wave Single Side Cooled	0.0983	0.0976	0.0971	0.0966	0.0953		

Form Factors							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave	3.464	2.449	2	1.732	1.414	1.149	1
Sine wave	3.98	2.778	2.22	1.879	1.57		

11.2 Calculating  $V_T$  using ABCD Coefficients

The on-state characteristic  $I_T$  vs.  $V_T$ , on page 6 is represented in two ways;

- (i) the well established  $V_{T0}$  and  $r_T$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_T$  in terms of  $I_T$  given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for  $V_T$  agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		115°C Coefficients	
A	0.2103143	A	-0.8731799
B	0.4754865	B	0.650089
C	$2.581886 \times 10^{-3}$	C	$3.072624 \times 10^{-3}$
D	-0.09062255	D	-0.08511282

## 11.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left( 1 - e^{-\frac{t}{\tau_p}} \right)$$

Where  $p = 1$  to  $n$ ,  $n$  is the number of terms in the series and:

- $t$  = Duration of heating pulse in seconds.
- $r_t$  = Thermal resistance at time  $t$ .
- $r_p$  = Amplitude of  $p_{th}$  term.
- $\tau_p$  = Time Constant of  $r_{th}$  term.

The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled				
Term	1	2	3	4
$r_p$	0.02328162	0.0165603	$4.739431 \times 10^{-3}$	$1.805256 \times 10^{-3}$
$\tau_p$	0.7294482	0.1571701	0.0343812	0.01059348

D.C. Single Side Cooled				
Term	1	2	3	4
$r_p$	0.06051172	0.0215729	$9.551987 \times 10^{-3}$	$2.562011 \times 10^{-3}$
$\tau_p$	5.722131	0.3167688	0.06285918	0.01198397

**Curves**

Figure 1 – On-state characteristics of Limit device

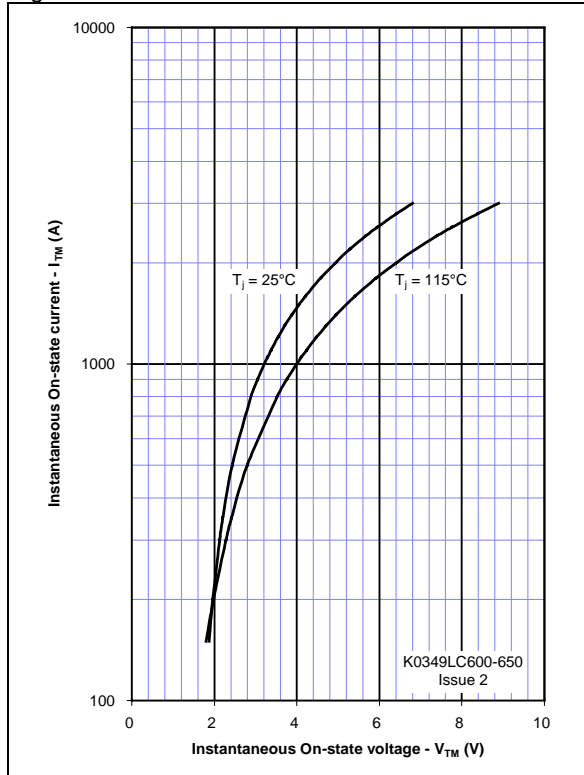


Figure 2 - Transient Thermal Impedance

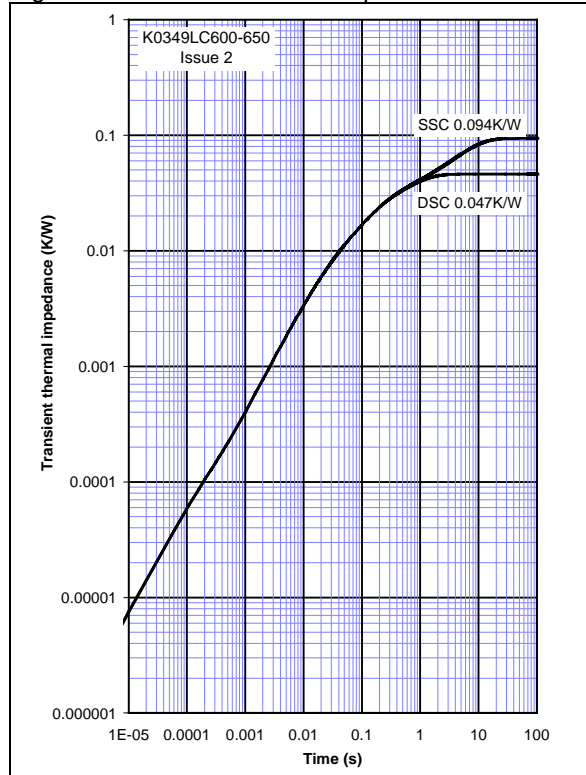


Figure 3 – Gate Characteristics - Trigger Limits

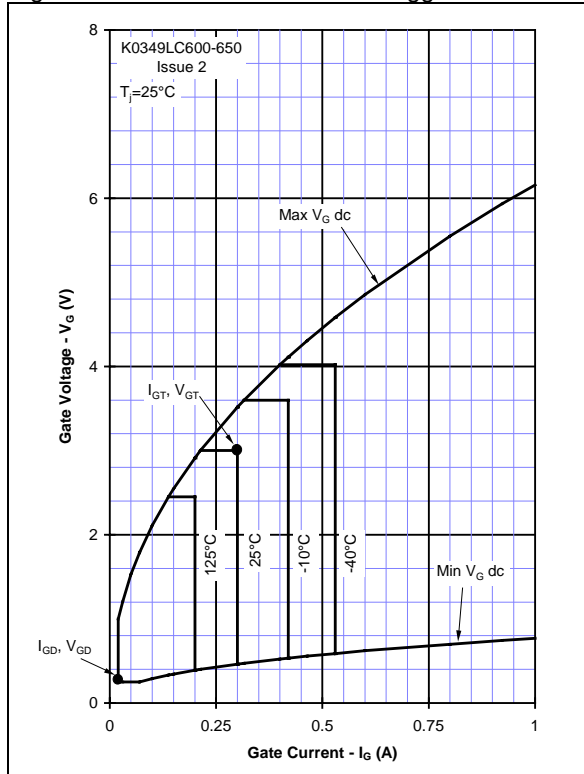


Figure 4 - Gate Characteristics - Power Curves

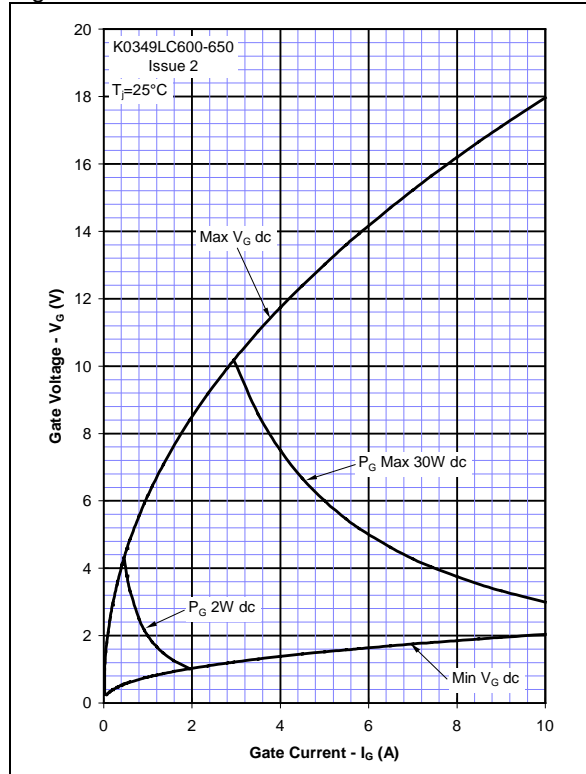


Figure 5 – Recovered Charge,  $Q_{rr}$

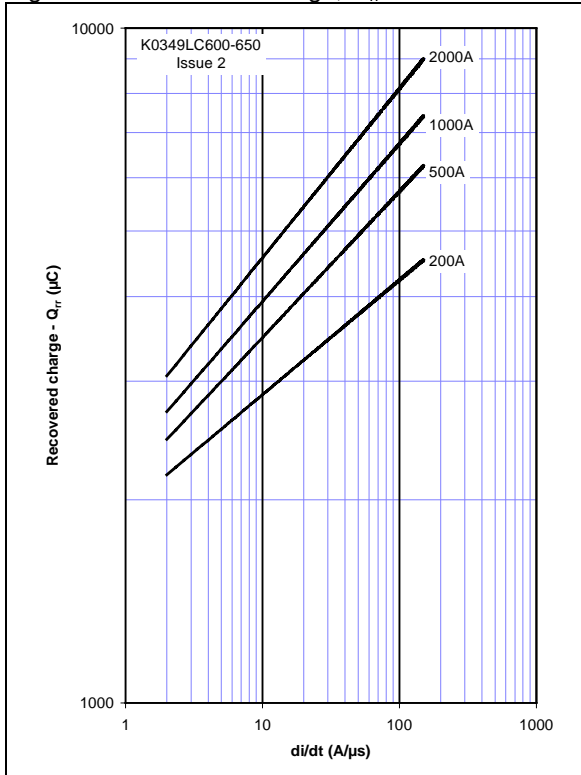


Figure 6 – Recovered charge,  $Q_{ra}$  (50% chord)

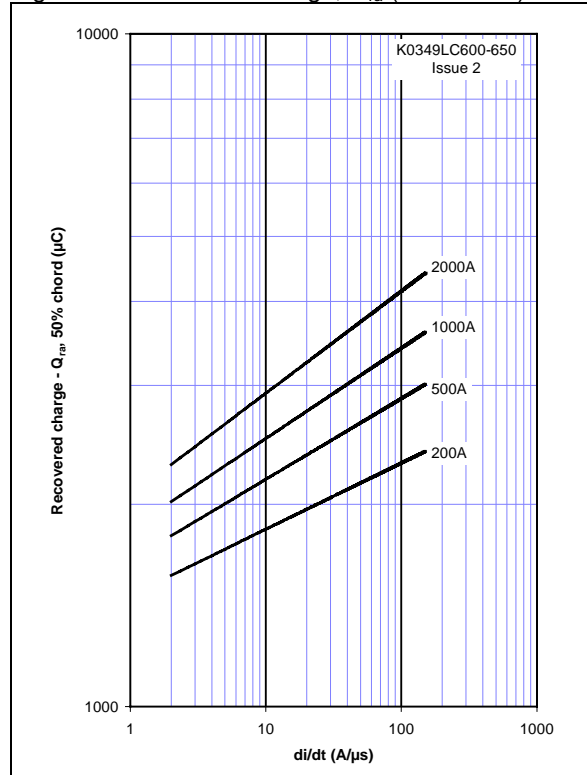


Figure 7 – Reverse recovery current,  $I_{rm}$

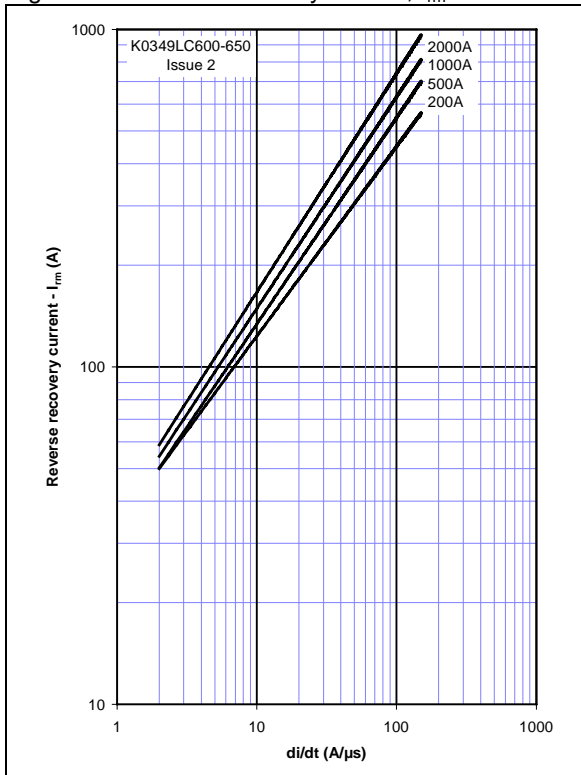


Figure 8 – Reverse recovery time,  $t_{rr}$

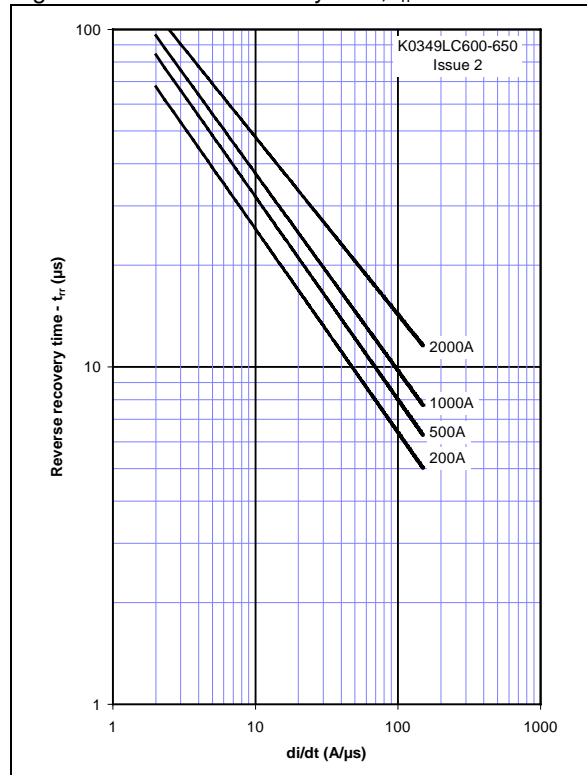


Figure 9 – On-state current vs. Power dissipation – Double Side Cooled (Sine wave)

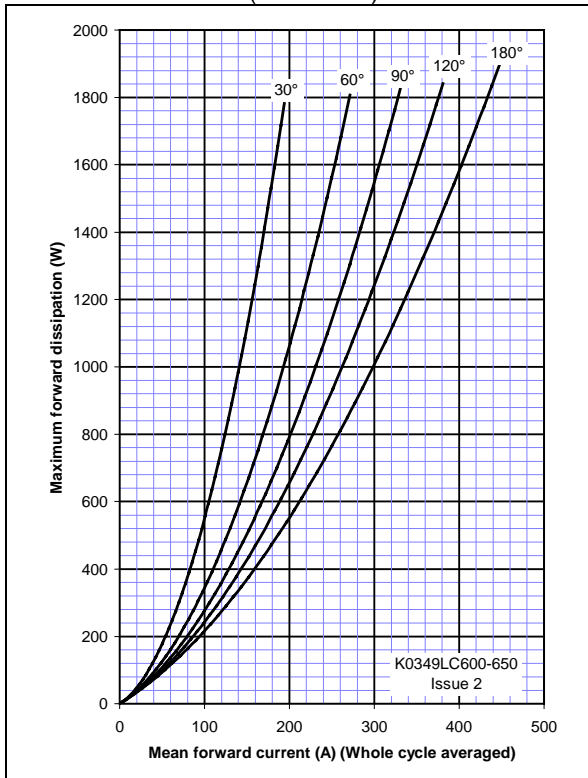


Figure 10 – On-state current vs. Heatsink temperature - Double Side Cooled (Sine wave)

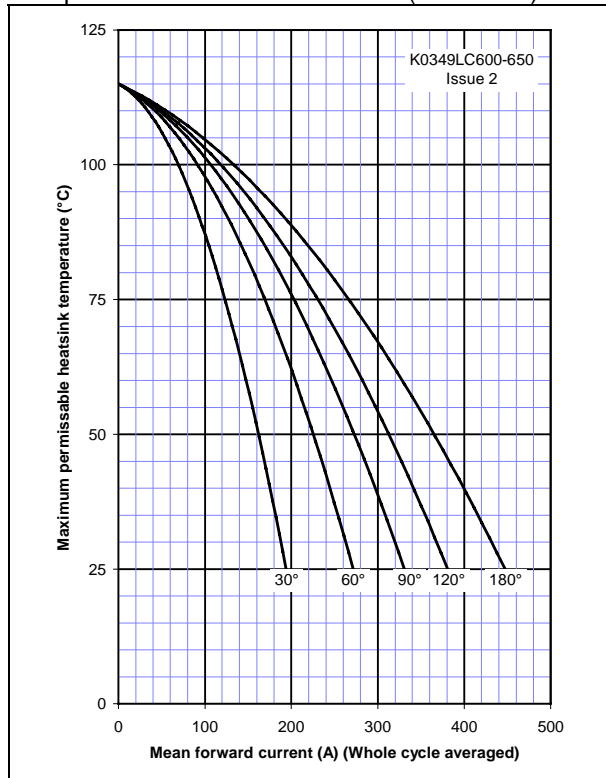


Figure 11 – On-state current vs. Power dissipation – Double Side Cooled (Square wave)

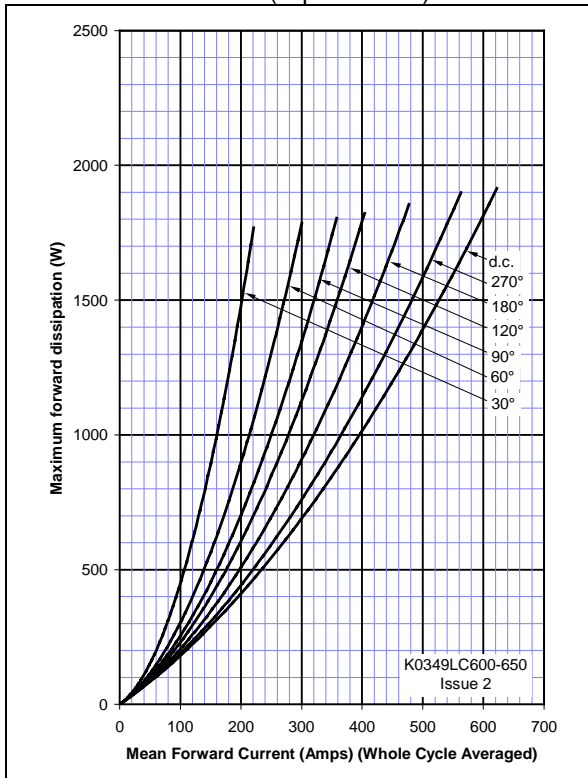


Figure 12 – On-state current vs. Heatsink temperature – Double Side Cooled (Square wave)

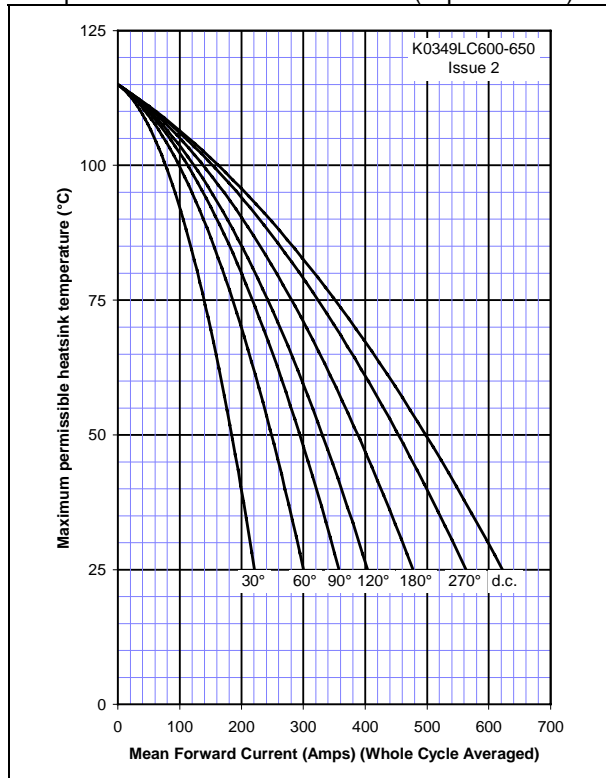




Figure 13 – On-state current vs. Power dissipation – Single Side Cooled (Sine wave)

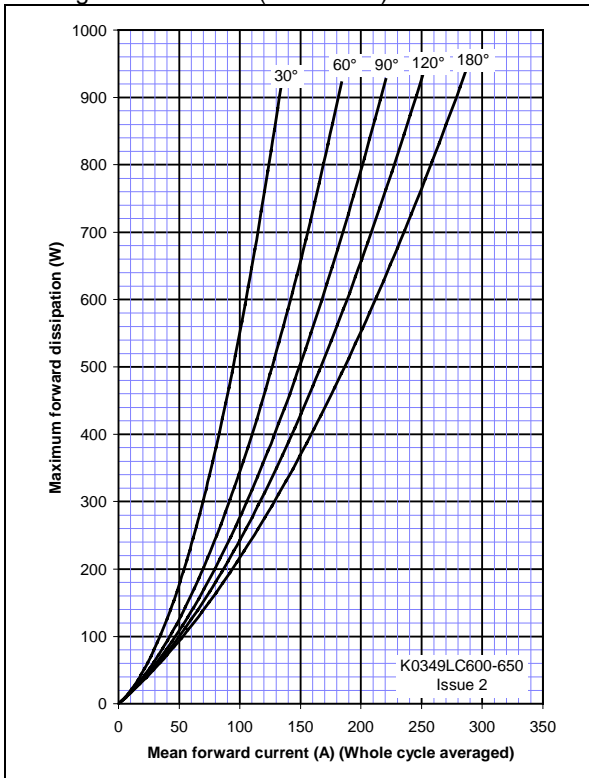


Figure 14 – On-state current vs. Heatsink temperature – Single Side Cooled (Sine wave)

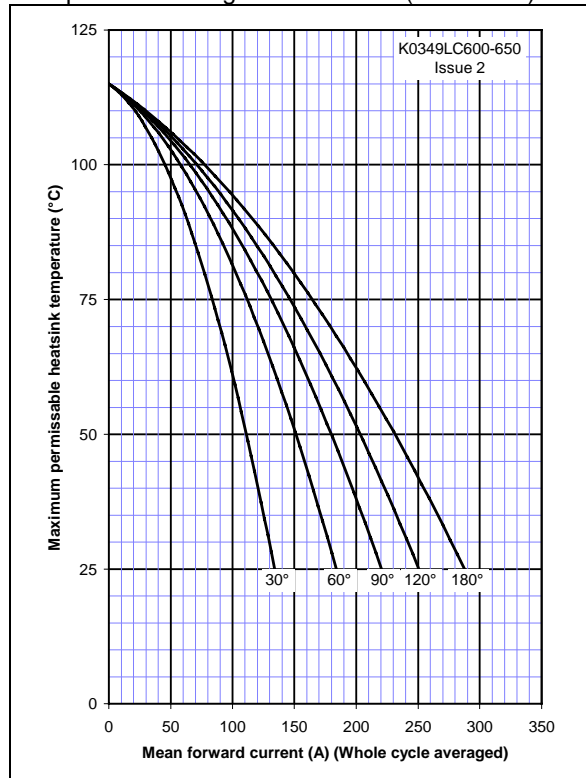


Figure 15 – On-state current vs. Power dissipation – Single Side Cooled (Square wave)

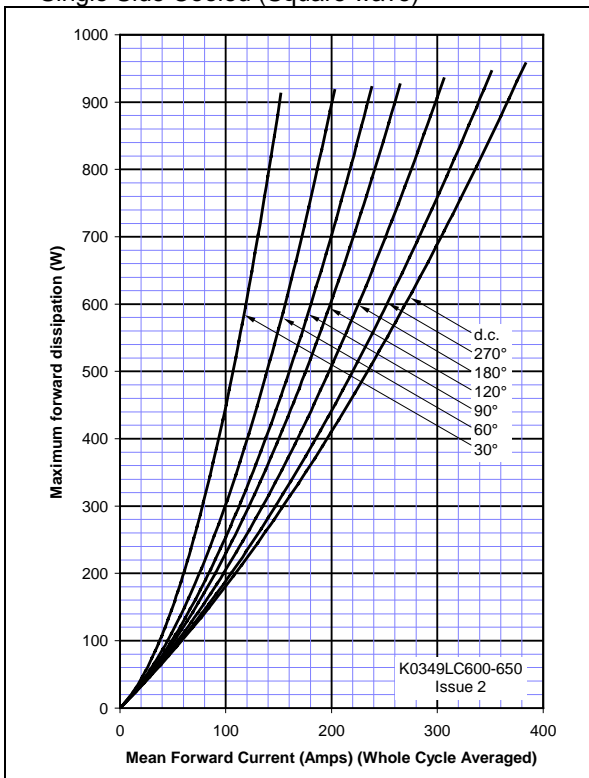


Figure 16 – On-state current vs. Heatsink temperature – Single Side Cooled (Square wave)

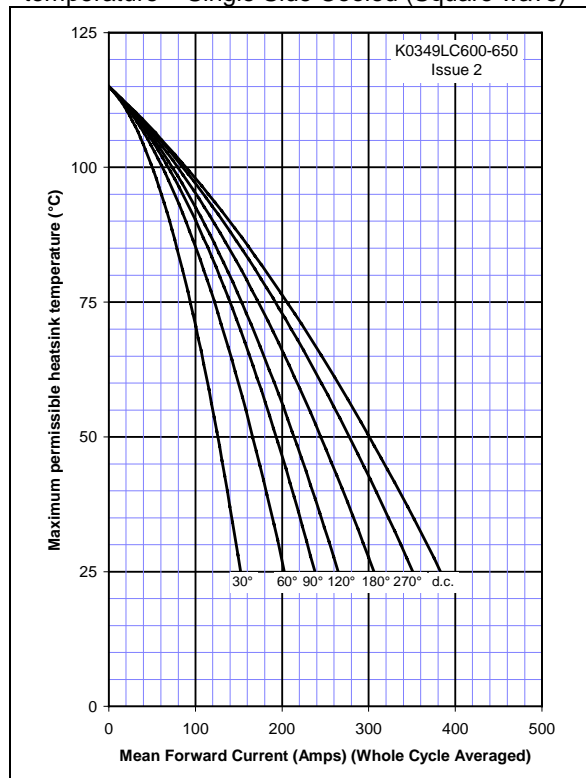


Figure 17 – Square Wave Frequency Ratings

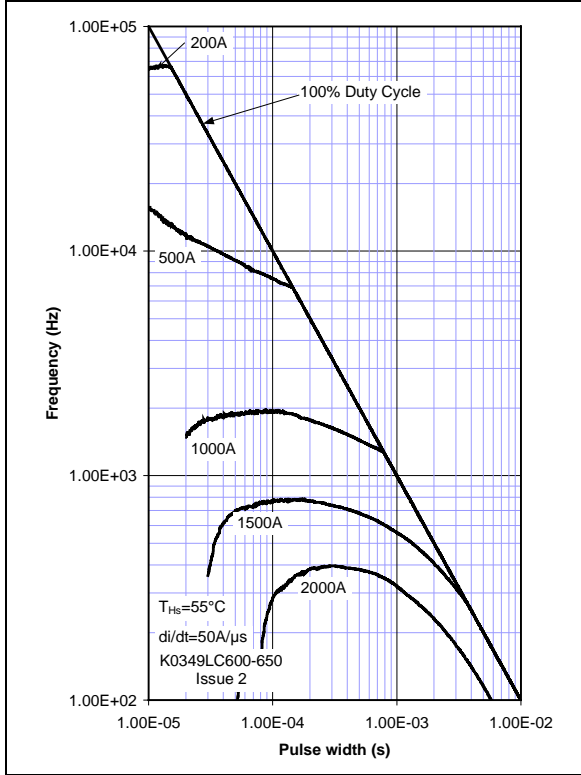


Figure 18 – Sine Wave Frequency Ratings

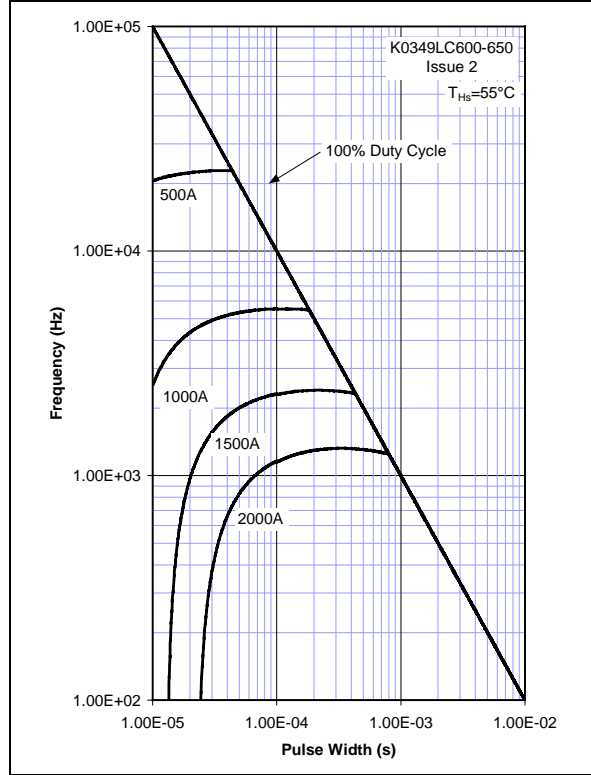
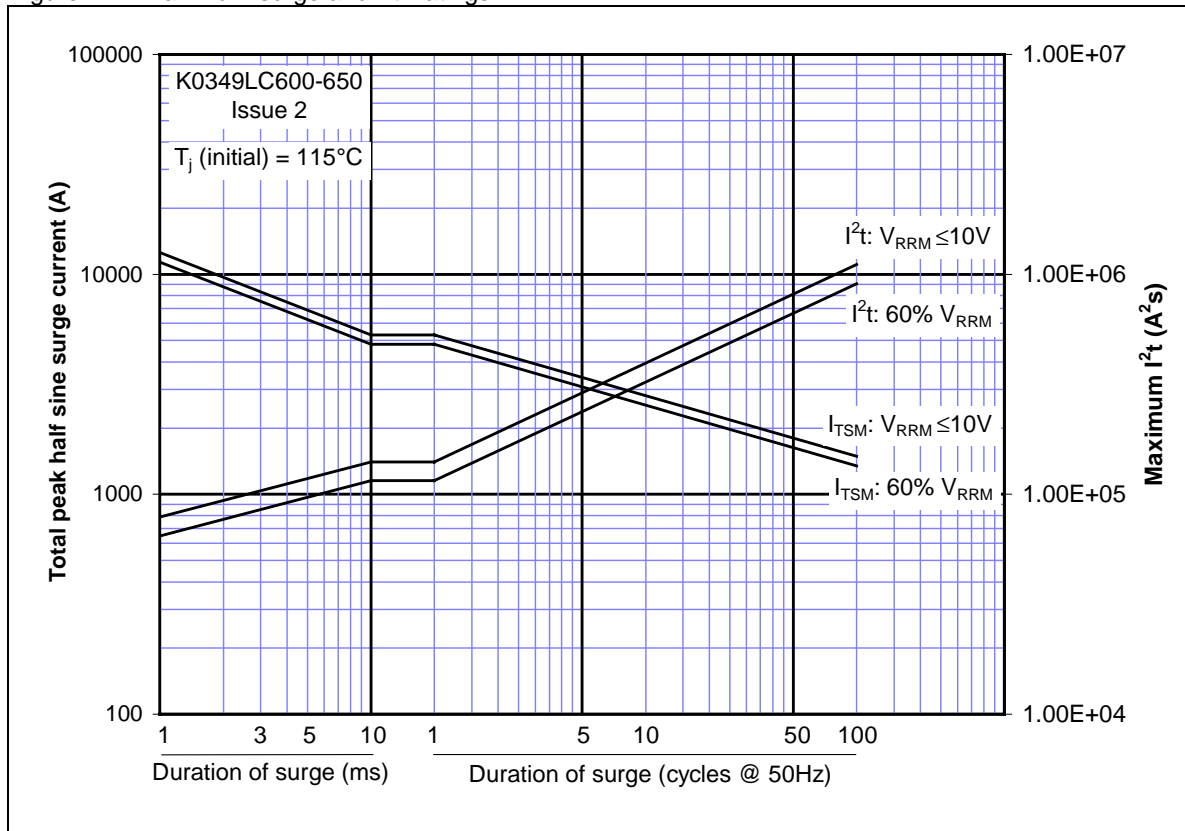
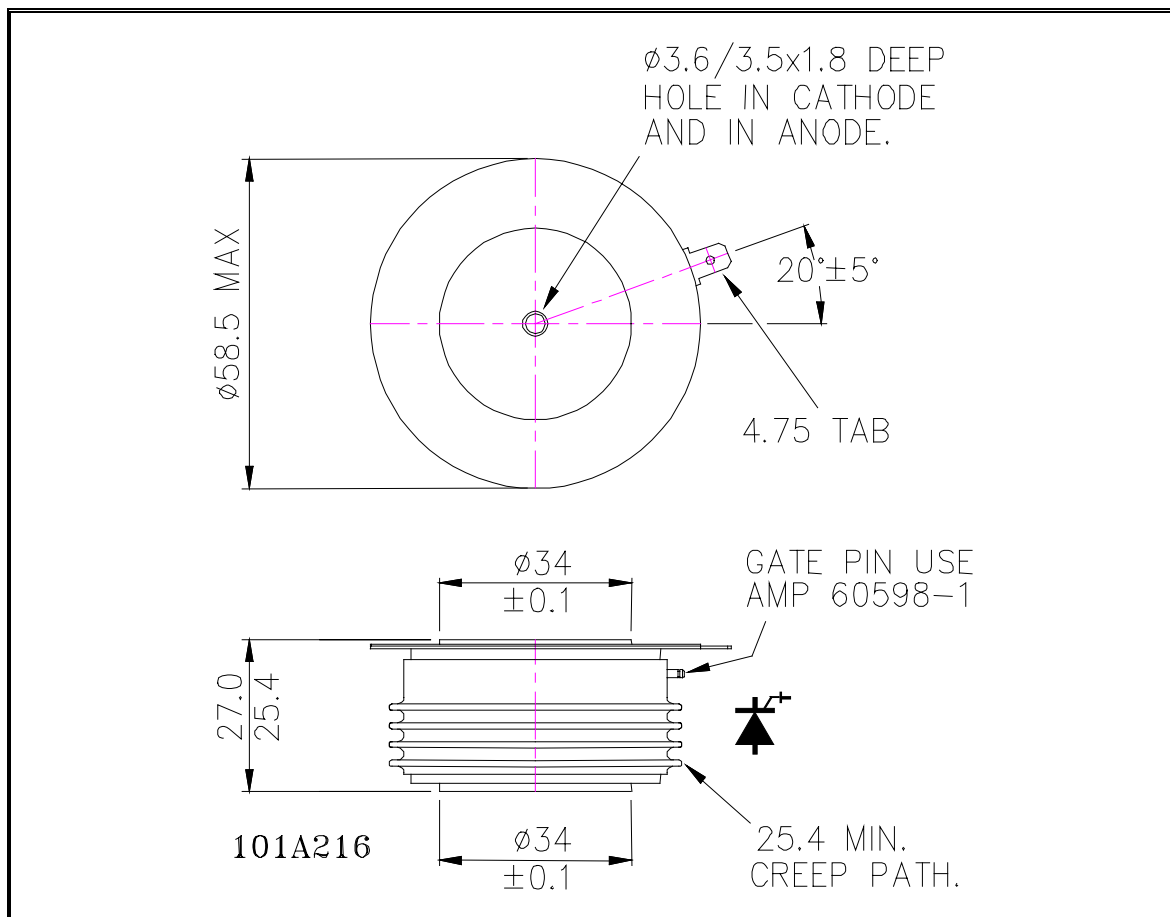


Figure 17 – Maximum surge and  $I^2t$  Ratings



**Outline Drawing & Ordering Information**



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Fixed Type Code	Fixed Outline Code	Voltage Code 60-65	Fixed turn-off time code

Typical order code: K0349LC640 – 6400V  $V_{DRM}$ ,  $V_{RRM}$ , 27mm clamp height capsule.

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