

TOSHIBA INTEGRATED IGBT MODULE SILICON N CHANNEL IGBT

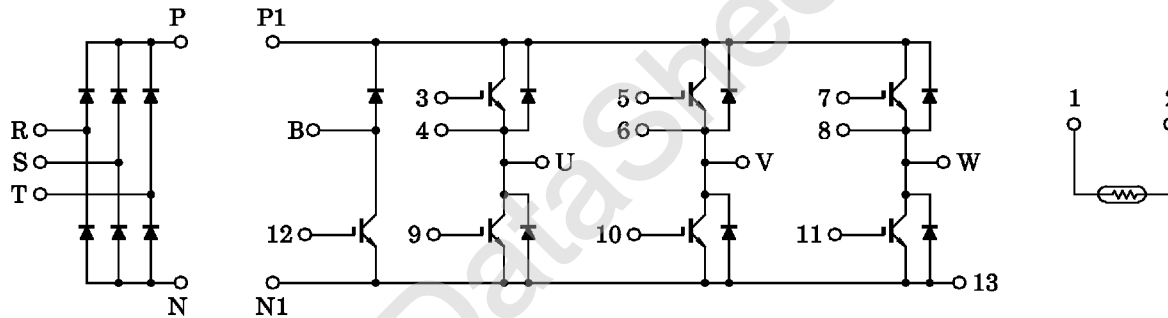
MIG25Q906H, MIG25Q906HA

HIGH POWER SWITCHING APPLICATIONS

MOTOR CONTROL APPLICATIONS

- Integrates Inverter, Converter and Brake Power Circuits and Thermistor in One Package.
- Output (Inverter Stage) : 3 ϕ 25 A / 1200 V IGBT
- Input (Converter Stage) : 3 ϕ 20 A / 1600 V Silicon Rectifier
- The Electrodes are Isolated from Case.
- Weight : 190 g
- Outline
 - MIG25Q906H : 2-108E5A
 - MIG25Q906HA : 2-108E6A

EQUIVALENT CIRCUIT



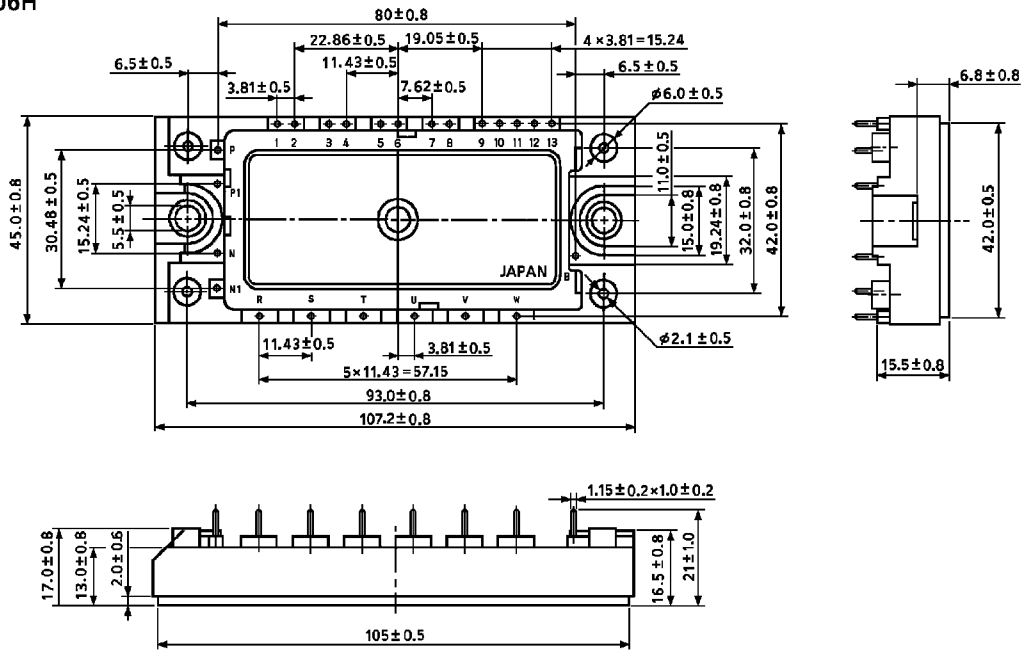
961001EAA2

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

Package Dimension

Unit : mm

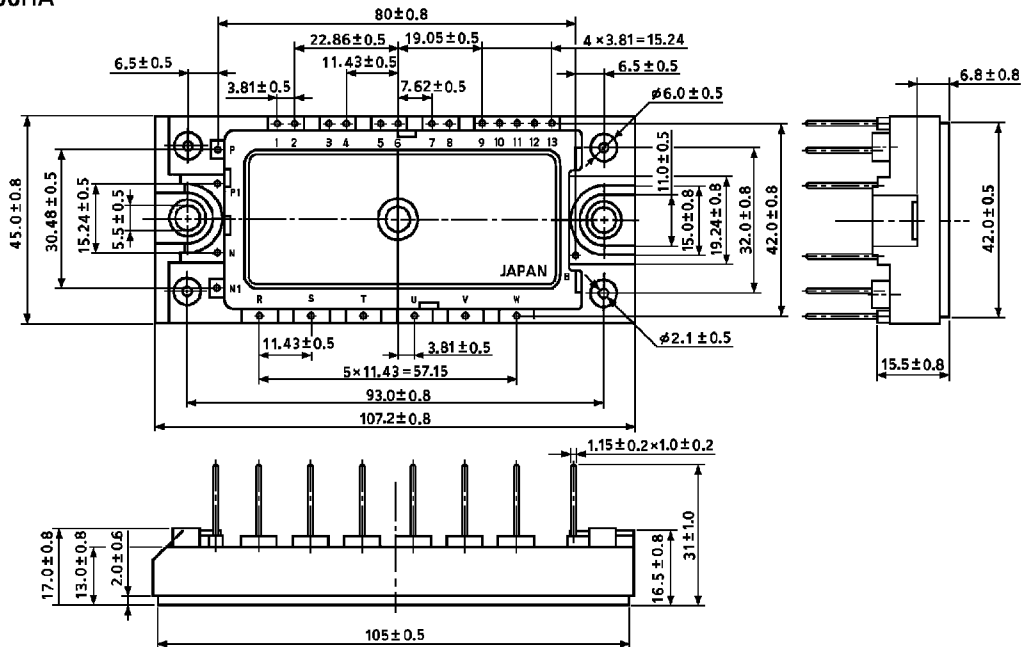
MIG25Q906H



2-108E5A

Unit : mm

MIG25Q906HA



2-108E6A

MAXIMUM RATINGS (Ta = 25°C)

STAGE		CHARACTERISTIC		SYMBOL	RATING	UNIT	
Inverter	Collector-Emitter Voltage			V _{CES}	1200	V	
	Gate-Emitter Voltage			V _{GES}	±20	V	
	Collector Current	DC		I _C	35 / 25	A	
		1 ms		I _{CP}	70 / 50	A	
	Forward Current	DC		I _F	25	A	
		1 ms		I _{FM}	50	A	
Collector Power Dissipation (Tc = 25°C)			P _C	200	W		
Converter	Repetitive Peak Reverse Voltage			V _{RRM}	1600	V	
	Average Output Rectified Current			I _O	20	A	
	Peak One Cycle Surge Forward Current (50 Hz, Non-Repetitive)			I _{FSM}	400	A	
Brake	IGBT	Collector-Emitter Voltage		V _{CES}	1200	V	
		Gate-Emitter Voltage		V _{GES}	±20	V	
		Collector Current	DC		I _C	35 / 25	A
			1 ms		I _{CP}	70 / 50	A
	Collector Power Dissipation (Tc = 25°C)			P _C	200	W	
	FWD	Reverse Voltage			V _R	1200	V
		Forward Current	DC		I _F	25	A
			1 ms		I _{FM}	50	A
Module		Junction Temperature			T _j	150	°C
	Storage Temperature Range			T _{stg}	-40~125	°C	
	Isolation Voltage			V _{Isol}	2500 (AC 1 minute)	V	
	Screw Torque			—	6	N·m	

(25°C / 80°C)
(25°C / 80°C)

(25°C / 80°C)
(25°C / 80°C)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

a. Inverter stage

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current		I_{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0$	—	—	± 500	nA	
Collector Cut-Off Current		I_{CES}	$V_{CE} = 1200 \text{ V}, V_{GE} = 0$	—	—	0.5	mA	
Gate-Emitter Cut-Off Voltage		$V_{GE} \text{ (off)}$	$I_C = 25 \text{ mA}, V_{CE} = 5 \text{ V}$	—	6.0	—	V	
Collector-Emitter Saturation Voltage		$V_{CE} \text{ (sat)}$	$I_C = 25 \text{ A}$	$T_j = 25^\circ\text{C}$	—	2.8	3.2	V
			$V_{GE} = 15 \text{ V}$	$T_j = 125^\circ\text{C}$	—	3.1	3.7	
Input Capacitance		C_{ies}	$V_{CE} = 10 \text{ V}, V_{GE} = 0,$ $f = 1 \text{ MHz}$	—	2600	—	pF	
Switching Time	Rise Time	t_r	$V_{CC} = 600 \text{ V}$	—	0.07	0.15	μs	
	Turn-On Time	t_{on}	$I_C = 25 \text{ A}$	—	0.15	0.30		
	Fall Time	t_f	$V_{GE} = \pm 15 \text{ V}$	—	0.07	0.10		
	Turn-Off Time	t_{off}	$R_G = 51 \Omega$ $T_j = 125^\circ\text{C}$ (Note 1)	—	0.60	0.90		
Forward Voltage		V_F	$I_F = 25 \text{ A}, V_{GE} = 0$	—	2.0	2.8	V	
Reverse Recovery Time		t_{rr}	$I_F = 25 \text{ A}, V_{GE} = -10 \text{ V}$ $di/dt = 400 \text{ A}/\mu\text{s}$	—	0.10	0.25	μs	
Thermal Resistance		$R_{th(j-c)}$	Transistor	—	—	0.6	$^\circ\text{C}/\text{W}$	
			Diode	—	—	1.0		

b. Converter stage

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Repetitive Peak Reverse Current		I_{RRM}	$V_{RRM} = 1600 \text{ V}$	—	—	50	μA
Peak Forward Voltage		V_{FM}	$I_{FM} = 20 \text{ A}$	—	1.05	1.20	V
Peak One Cycle Surge Forward Current		I_{FSM}	50 Hz sine-half-wave	400	—	—	A
Thermal Resistance		$R_{th(j-c)}$	—	—	—	1.56	$^\circ\text{C}/\text{W}$

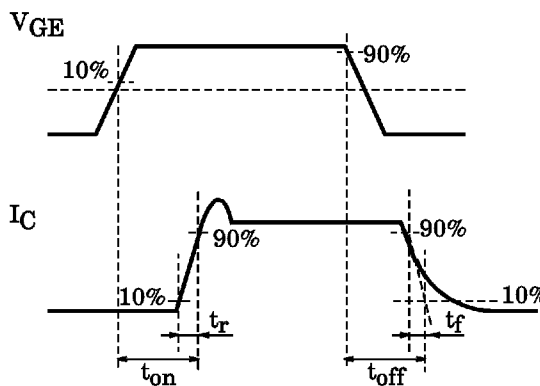
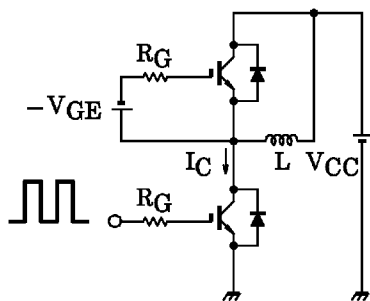
c. Brake stage

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current		I_{GES}	$V_{GE} = \pm 20\text{ V}, V_{CE} = 0$	—	—	± 500	nA	
Collector Cut-Off Current		I_{CES}	$V_{CE} = 1200\text{ V}, V_{GE} = 0$	—	—	0.5	mA	
Reverse Current		I_R	$V_R = 1200\text{ V}$	—	—	1.0	mA	
Gate-Emitter Cut-Off Voltage		$V_{GE}(\text{off})$	$I_C = 25\text{ mA}, V_{CE} = 5\text{ V}$	—	6.0	—	V	
Collector-Emitter Saturation Voltage		$V_{CE}(\text{sat})$	$I_C = 25\text{ A}$	$T_j = 25^\circ\text{C}$	—	2.8	3.2	V
			$V_{GE} = 15\text{ V}$	$T_j = 125^\circ\text{C}$	—	3.1	3.7	
Input Capacitance		C_{ies}	$V_{CE} = 10\text{ V}, V_{GE} = 0,$ $f = 1\text{ MHz}$	—	2600	—	pF	
Switching Time	Rise Time	t_r	$V_{CC} = 600\text{ V}$	—	0.07	0.15	μs	
	Turn-On Time	t_{on}	$I_C = 25\text{ A}$	—	0.15	0.30		
	Fall Time	t_f	$V_{GE} = \pm 15\text{ V}$	—	0.07	0.10		
	Turn-Off Time	t_{off}	$R_G = 51\ \Omega$ $T_j = 125^\circ\text{C}$ (Note 1)	—	0.60	0.90		
Forward Voltage		V_F	$I_F = 25\text{ A}, V_{GE} = 0$	—	2.0	2.8	V	
Thermal Resistance		$R_{th(j-c)}$	Transistor	—	—	0.6	$^\circ\text{C/W}$	
			Diode	—	—	1.0		

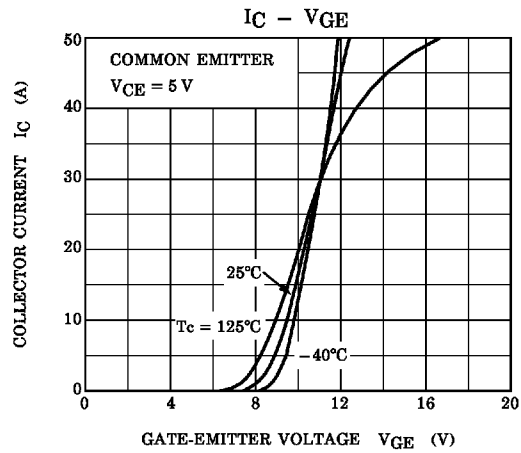
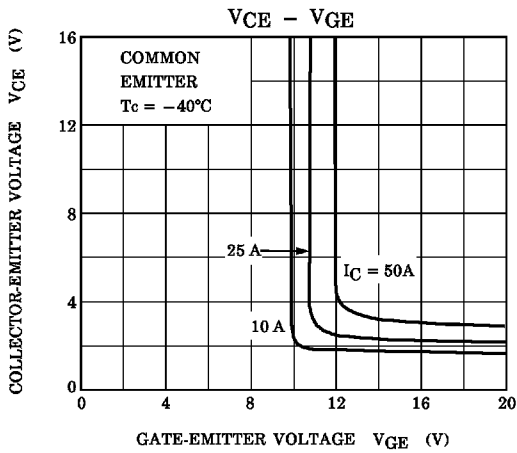
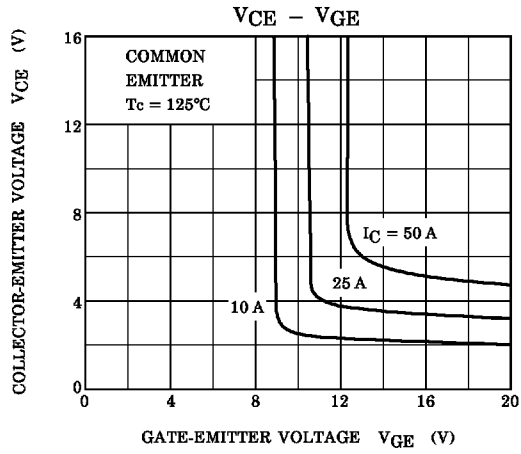
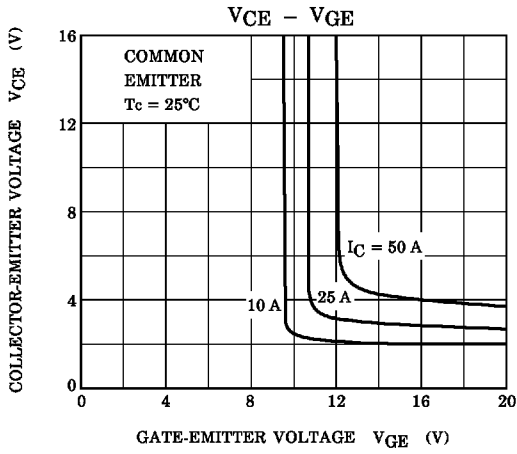
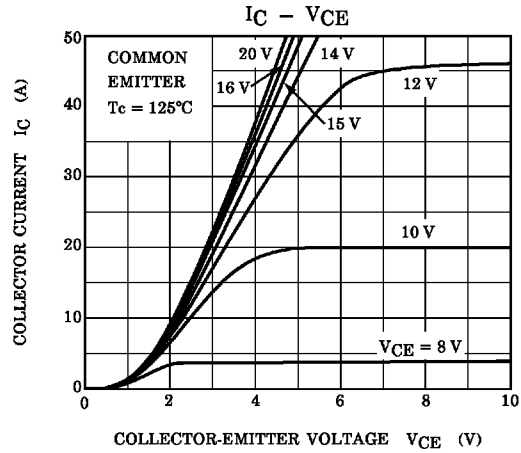
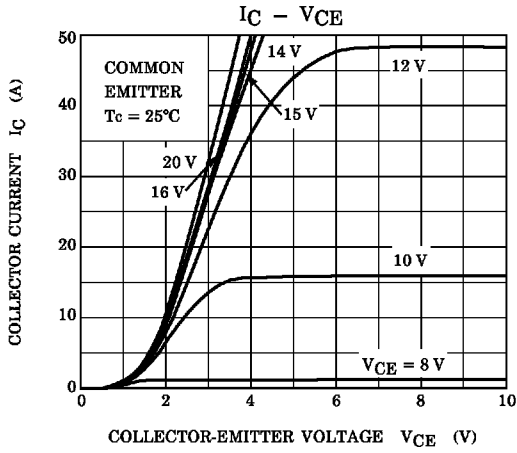
d. Thermistor

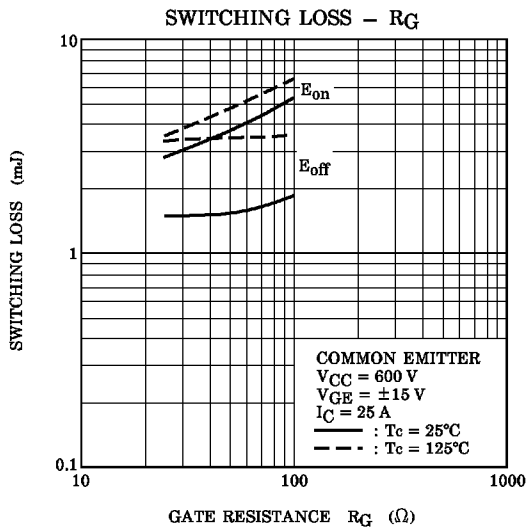
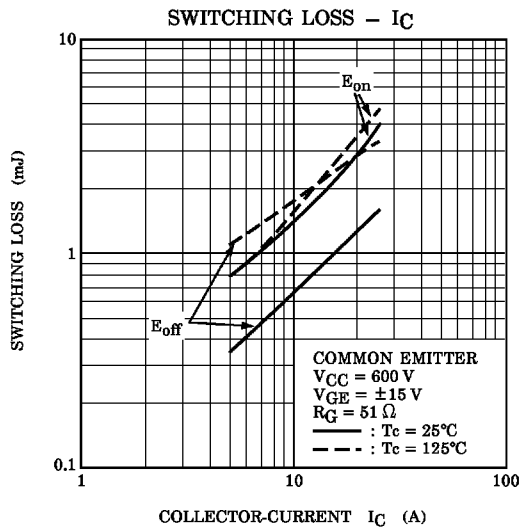
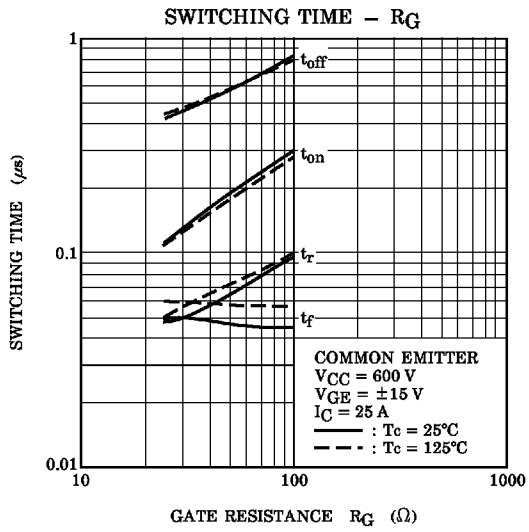
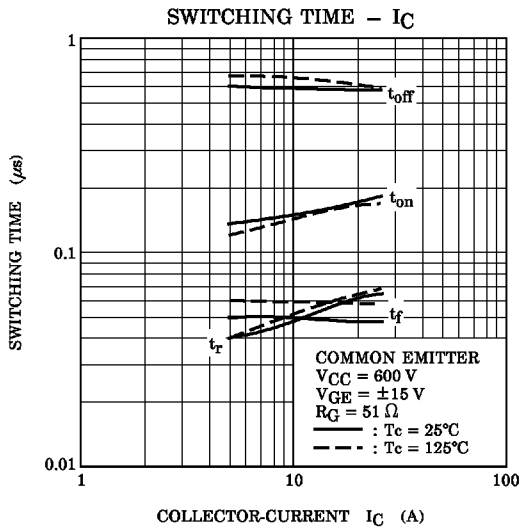
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Zero-power Resistance	R_{25}	$I_{TM} = 0.2\text{ mA}, T_c = 25^\circ\text{C}$	17.31	20	23.14	$\text{k}\Omega$
B Value	$B_{25/85}$	$T_c = 25^\circ\text{C} / T_c = 85^\circ\text{C}$	—	3760	—	K

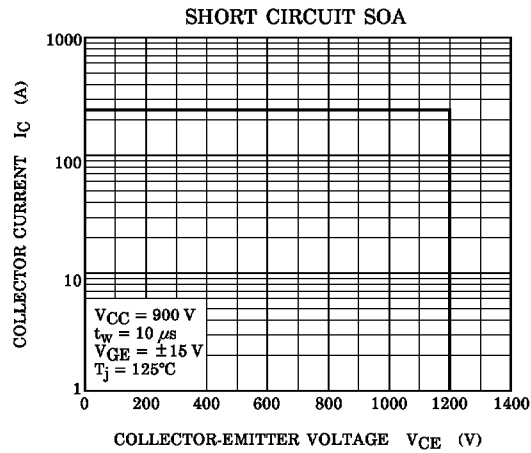
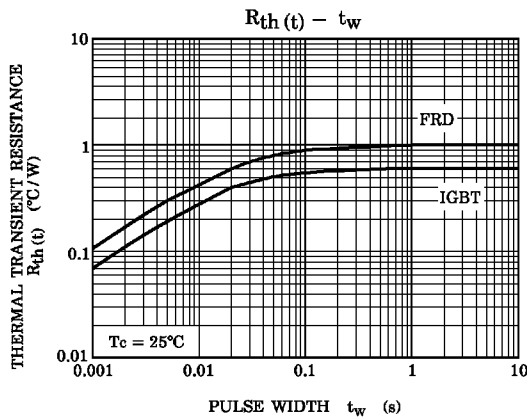
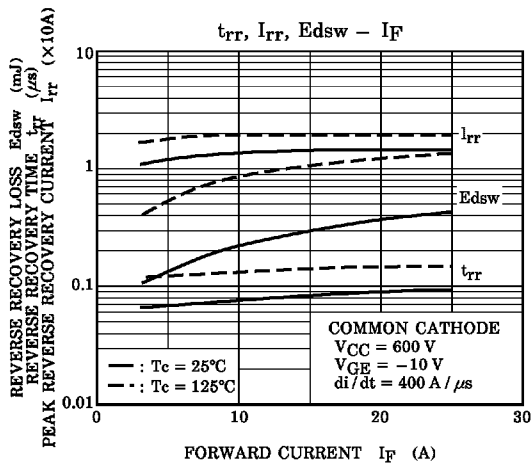
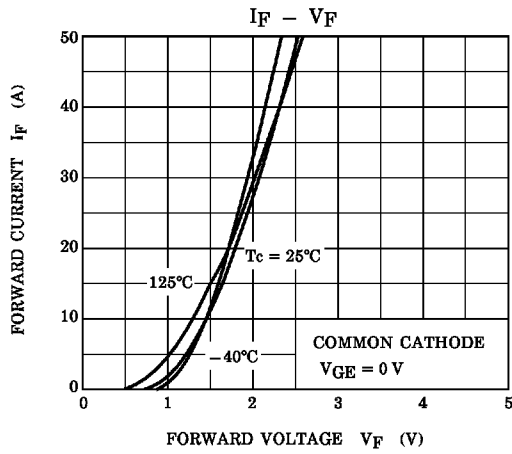
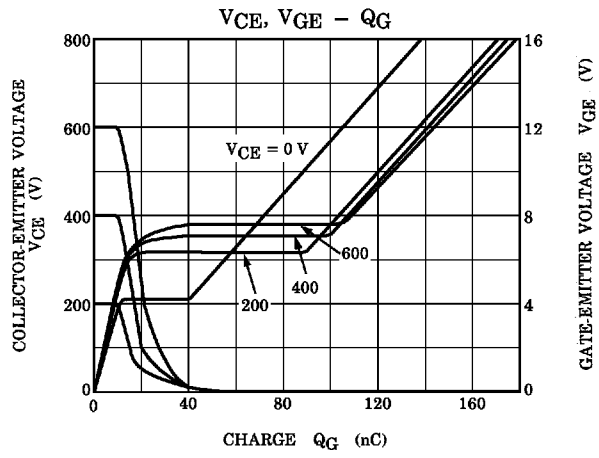
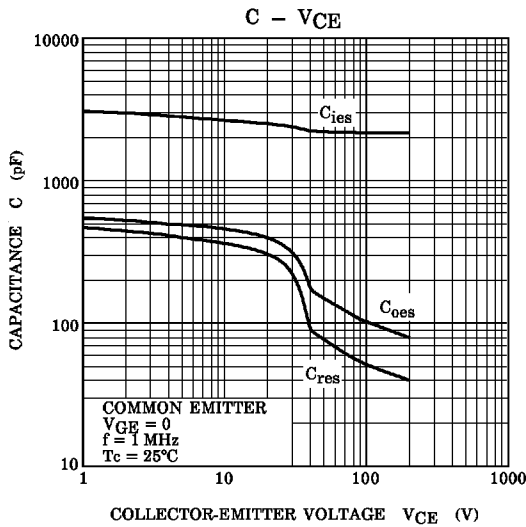
(Note 1) : Switching Time Test Circuit & Timing Chart

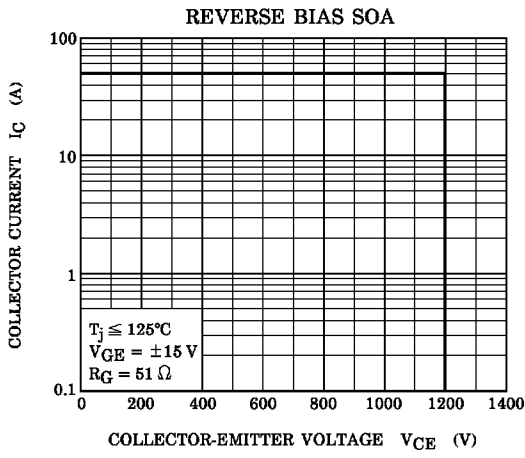


a. Inverter stage/c. Brake stage









b. Converter stage

