# TOSHIBA

TOSHIBA Power Transistor Module Silicon NPN Triple Diffused Type (Darlington power transistor 4 in 1)

# MP4513

High Power Switching Applications.

Hammer Drive, Pulse Motor Drive and Inductive Load Switching.

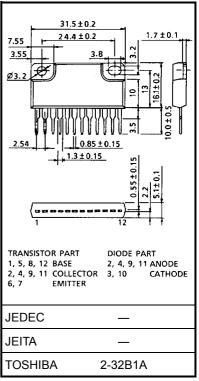
- Package with heat sink isolated to lead (SIP 12 pin)
- High collector power dissipation (4 devices operation) :  $P_T = 5 \text{ W} (Ta = 25^{\circ}\text{C})$
- High collector current:  $I_{C(DC)} = 5 A (max)$
- High DC current gain:  $h_{FE} = 1000$  (min) ( $V_{CE} = 3 \text{ V}$ ,  $I_{C} = 3 \text{ A}$ )
- Diode included for absorbing fly-back voltage.

Characteristics		Symbol	Rating	Unit
Collector-base voltage		V <sub>CBO</sub>	100	V
Collector-emitter voltage		V <sub>CEO</sub>	100	V
Emitter-base voltage		V <sub>EBO</sub>	5	V
Collector current	DC	Ι <sub>C</sub>	5	Α
	Pulse	I <sub>CP</sub>	8	
Continuous base current		Ι <sub>Β</sub>	0.1	А
Collector power dissipation		Pc	3.0	W
(1 device operation, Ta = 25°C)				
Collector power dissipation	Ta = 25°C	Рт	5.0	W
(4 devices operation)	Tc = 25°C		25	vv
Isolation voltage		V <sub>Isol</sub>	1000	V
Junction temperature		Тј	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C

#### Maximum Ratings (Ta = 25°C)

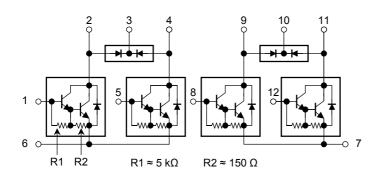
Industrial Applications





Weight: 6.0 g (typ.)

## Array Configuration



#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance of junction to ambient	ΣR <sub>th (j-a)</sub>	25	°C/W	
(4 devices operation, Ta = 25°C)				
Thermal resistance of junction to case	5 <b>D</b>	5.0	°CW	
(4 devices operation, $Tc = 25^{\circ}C$ )	ΣR <sub>th (j-c)</sub>	5.0	C/VV	
Maximum lead temperature for soldering purposes	TL	260	°C	
(3.2 mm from case for 10 s)				

#### **Electrical Characteristics (Ta = 25°C)**

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off cu	rrent	I <sub>CBO</sub>	V <sub>CB</sub> = 100 V, I <sub>E</sub> = 0 A	_	_	10	μA
Collector cut-off cu	rrent	ICEO	V <sub>CE</sub> = 100 V, I <sub>B</sub> = 0 A	_	_	10	μA
Emitter cut-off curr	ent	I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0 A	0.3	—	2.0	mA
Collector-base brea	akdown voltage	V (BR) CBO	I <sub>C</sub> = 1 mA, I <sub>E</sub> = 0 A	100	_	_	V
Collector-emitter b	reakdown voltage	V (BR) CEO	I <sub>C</sub> = 30 mA, I <sub>B</sub> = 0 A	100	_	_	V
DC ourrent goin		h <sub>FE (1)</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 0.5 A	1000	_	_	
DC current gain	h <sub>FE (2)</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 3 A	1000	_	_	—	
Saturation voltage	Collector-emitter	V <sub>CE (sat)</sub>	I <sub>C</sub> = 3 A, I <sub>B</sub> = 12 mA	_	_	2.0	v
Saturation voltage	Base-emitter	V <sub>BE (sat)</sub>	I <sub>C</sub> = 3 A, I <sub>B</sub> = 12 mA	_	_	2.5	
Transition frequence	су	f <sub>T</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 0.5 A	_	10	_	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 A, f = 1 MHz	_	40	_	pF
Turn-on time Switching time Fall time	t <sub>on</sub>	Input → UTPut	_	0.5	_		
	Storage time	t <sub>stg</sub>		_	4.0	_	μs
	Fall time	t <sub>f</sub>	l <sub>B1</sub> = −l <sub>B2</sub> = 12 mA, duty cycle ≤ 1%	_	2.5	_	

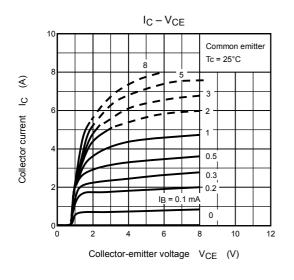
### Emitter-Collector Diode Ratings and Characteristics (Ta = 25°C)

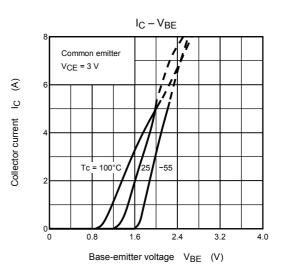
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward current	I <sub>FM</sub>	—	_	_	5	А
Surge current	I <sub>FSM</sub>	t = 1 s, 1 shot	_	_	8	А
Forward voltage	VF	I <sub>F</sub> = 1 A, I <sub>B</sub> = 0 A	_	1.1	1.8	V
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 5 A, V <sub>BE</sub> = −3 V, dI <sub>F</sub> /dt = −50 A/µs	_	3.0	_	μs
Reverse recovery charge	Q <sub>rr</sub>			40		μC

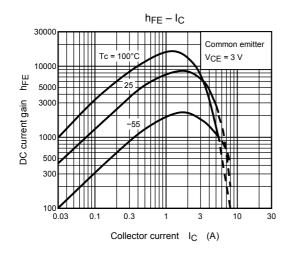
## Flyback-Diode Rating and Characteristics (Ta = 25°C)

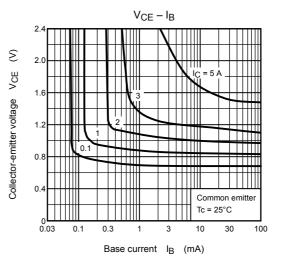
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward current	I <sub>FM</sub>	—	_	_	3	А
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 100 V	_	_	0.4	μA
Reverse voltage	V <sub>R</sub>	I <sub>R</sub> = 100 μA	100	-	_	V
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 1 A	_	_	1.8	V

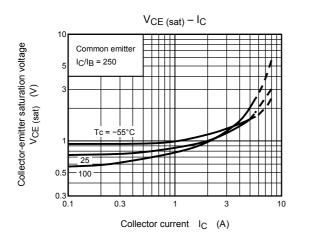
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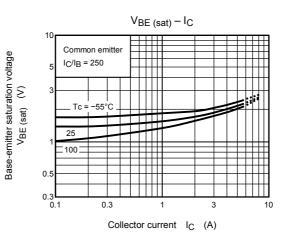


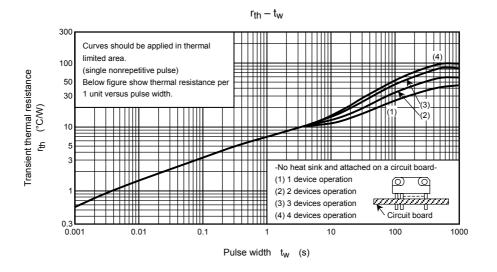


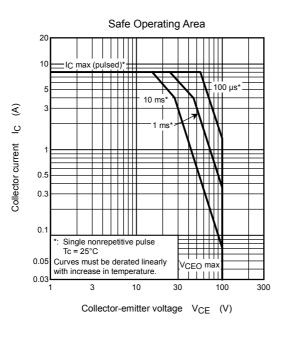


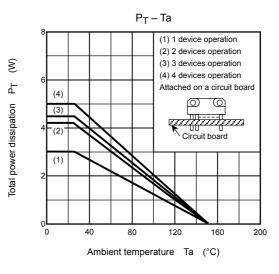


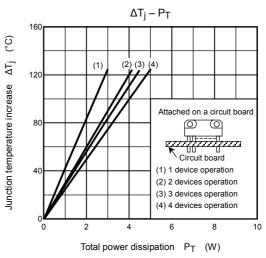












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