TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

$\begin{array}{c} \mathsf{TD62001P}, \mathsf{TD62001AP}, \mathsf{TD62001F}, \mathsf{TD62001AF}, \mathsf{TD62002P} \\ \mathsf{TD62002AP}, \mathsf{TD62002F}, \mathsf{TD62002AF}, \mathsf{TD62003P}, \mathsf{TD62003AP}, \mathsf{TD62004P}, \mathsf{TD62004P}, \mathsf{TD62004AF}, \mathsf{TD62004AF} \\ \end{array}$

7CH DARLINGTON SINK DRIVER

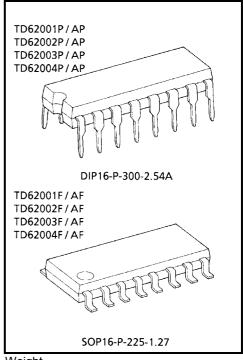
The TD62001P / AP / F / AF Series are high–voltage, high–current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads.

Applications include relay, hammer, lamp and display (LED) drivers.

FEATURES

- Output current (single output) 500 mA MAX.
- High sustaining voltage output 35 V MIN. (TD62001P / F Series) 50 V MIN. (TD62001AP / AF Series)
- Output clamp diodes
- Inputs compatible with various types of logic
- Package Type-P, AP: DIP-16 pin
- Package Type-F, AF: SOP-16 pin

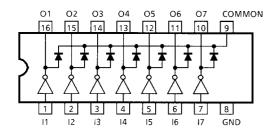
TYPE	INPUT BASE RESISTOR	DESIGNATION
TD62001P / AP / F / AF	External	General Purpose
TD62002P / AP / F / AF	10.5-kΩ + 7 V Zenner diode	14~25 V PMOS
TD62003P / AP / F / AF	2.7 kΩ	TTL, 5 V CMOS
TD62004P / AP / F / AF	10.5 kΩ	6~15 V PMOS, CMOS



Weight

DIP16-P-300-2.54A: 1.11 g (Typ.) SOP16-P-225-1.27: 0.16 g (Typ.)

PIN CONNECTION (TOP VIEW)

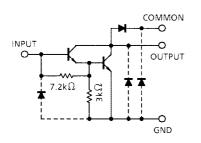


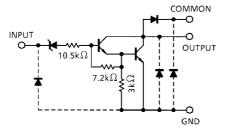
SCHEMATICS (EACH DRIVER)

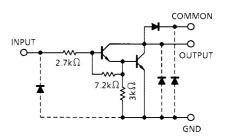
TD62001P / AP / F / AF

TD62002P / AP / F / AF

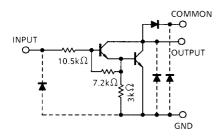
TD62003P / AP / F / AF







TD62004P / AP / F / AF



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTI	SYMBOL	RATING	UNIT			
Output Sustaining	P, F	V (-)	-0.5~35	V		
Voltage	AP, AF	V _{CE} (SUS)	-0.5~50	V		
Output Current		lout	500	mA / ch		
Input Voltage	V _{IN} (Note 1)	-0.5~30	V			
Input Current	I _{IN} (Note 2)	25	mA			
Clamp Diode	P, F	V _R	35	V		
Reverse Voltage	AP, AF	VR	50	V		
Clamp Diode Forward Currer	I _F	500	mA			
	Р		1.0	W		
Power Dissipation	AP	P _D	1.47			
	F, AF		0.54 / 0.625 (Note 3)			
Operating	Р	-	-30~75	°C		
Temperature	AP, F, AF	T _{opr}	-40~85	C		
Storage Temperature		T _{stg}	-55~150	°C		

Note 1: Except TD62001P / AP / F / AF
Note 2: Only TD62001P / AP / F / AF

Note 3: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)



RECOMMENDED OPERATING CONDITIONS

(Ta = -40~85°C and Ta = -30~75°C for only Type-P)

CHARACTER	CHARACTERISTIC SYMBOL CONDITION		MIN	TYP.	MAX	UNIT			
Output Sustaining	P, F	Va= (0110)			0	_	35	V	
Voltage	AP, AF	V _{CE} (SUS)			0	_	50	v	
	AP			Duty = 10%	0	_	370		
	AP			Duty = 50%	0	_	130		
Output Current	Р	la	T _{pw} = 25 ms 7 Circuits	Duty = 10%	0	_	295	mA /	
Output Current	P	l _{OUT}	Ta = 85°C T _i = 120°C	Duty = 50%	0	_	95	ch	
	F 4F		1) 120 0	Duty = 10%	0	_	233	-	
	F, AF			Duty = 50%	0	_	70		
Input Voltage	Except TD62001P / AP / F / AF	V _{IN}		•	0	_	24	V	
Input Voltage (Output On)	TD62002				14.5	_	24		
	TD62003 V _{IN (C}	V _{IN (ON)}	$I_{OUT} = 400 \text{ mA}$ $I_{h_{FE}} = 800$			_	24	V	
	TD62004				6.2	_	24		
Input Voltage (Output Off)	TD62001	VIN (OFF)			0	_	0.6		
	TD62002				0	_	7.4	V	
	TD62003				0	_	0.7		
	TD62004				0	_	1.0		
Input Current	Only TD62001	I _{IN}			0	_	10	mA	
Clamp Diode Reverse	P, F	M			_	_	35	V	
Voltage	AP, AF	V_R			_	_	50	ı v	
Clamp Diode Forward Current		l _F			_	_	350	mA	
	Р		Ta = 85°C		_	_	0.6		
Power Dissipation	AP	P_{D}	1a - 05 C		_	_	0.76	W	
	AF, F		Ta = 85°C	(Note)	_	_	0.325		

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Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)



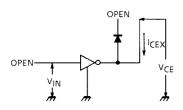
ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
	AP, AF			V _{CE} = 50 V, Ta = 25°C		_	_	50	
Output Leakage Current				V _{CE} = 50 V, Ta = 85°C		_	_	100	μΑ
	_			V _{CE} = 35 V, Ta = 25°C		_	_	50	
	F	I _{CEX}	1	V _{CE} = 35 V, Ta = 85°C		_	_	100	
	Б			V _{CE} = 35 V, Ta = 25°C		_	_	50	
	Р			V _{CE} = 35 V	V _{CE} = 35 V, Ta = 75°C		_	100	
				I _{OUT} = 350 mA, I _{IN} = 500 μA		_	1.3	1.6	
Collector-Emitter Saturation Voltage		V _{CE (sat)}	2	I _{OUT} = 200 mA, I _{IN} = 350 μA		_	1.1	1.3	V
				I _{OUT} = 100	mA, I _{IN} = 250 μA	_	0.9	1.1	
DC Current Transfer Ratio		h _{FE}	2	V _{CE} = 2 V,	I _{OUT} = 350 mA	1000	_	_	
	TD62002			V _{IN} = 20 V,	I _{OUT} = 350 mA	_	1.1	1.7	
Input Current (Output On)	TD62003	I _{IN (ON)}	3	V _{IN} = 2.4 V, I _{OUT} = 350 mA		_	0.4	0.7	mA
,	TD62004			V _{IN} = 9.5 V, I _{OUT} = 350 mA		_	0.8	1.2	
Input Current	Р		4	I _{OUT} = 500 μA, Ta = 75°C		50	65	_	
(Output Off)	AP, F, AF	IN (OFF)	4	I _{OUT} = 500 μA, Ta = 85°C		50	65	_	μA
	TD62002			V _{CE} = 2 V h _{FE} = 800	I _{OUT} = 350 mA	_	_	13.7	V
	1002002	Vin (on)	5		I _{OUT} = 200 mA	_	_	11.4	
Input Voltage	TD62003				I _{OUT} = 350 mA	_	_	2.6	
(Output On)					I _{OUT} = 200 mA	_	_	2.0	
					I _{OUT} = 350 mA	_	_	4.7	
					I _{OUT} = 200 mA	_	_	4.4	
	AP, AF	I _R	6	V _R = 50 V, Ta = 25°C		_	_	50	μΑ
				V _R = 50 V, Ta = 85°C		_	_	100	
Clamp Diode	F			V _R = 35 V, Ta = 25°C		_	_	50	
Reverse Current	ı			V _R = 35 V, Ta = 85°C		_	_	100	
	Р			V _R = 35 V, Ta = 25°C		_	_	50	
	'			V _R = 35 V, Ta = 75°C		_	_	100	
Clamp Diode Forward Volta	age	V _F	7	I _F = 350 mA		_	_	2.0	V
Input Capacitance		C _{IN} —			15	_	pF		
Turn-On Delay	P, F	tou	8	V_{OUT} = 35 V, R _L = 87.5 Ω C _L = 15 pF		_	0.1	_	- µs
	AP, AF	t _{ON}		V _{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF		_	0.1	_	
T 0#5.	P, F	torr	8	V_{OUT} = 35 V, R_{L} = 87.5 Ω C_{L} = 15 pF		_	0.2	_	
Turn-Off Delay	AP, AF	toff	0	V _{OUT} = 50 C _L = 15 pF	V, R _L = 125 Ω	_	0.2	_	

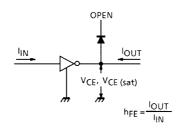
4

TEST CIRCUIT

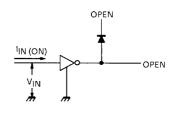
1. ICEX



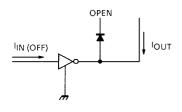
2. V_{CE (sat)}, h_{FE}



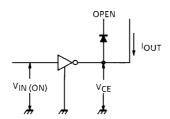
3. IIN (ON)



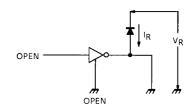
4. I_{IN (OFF)}



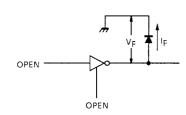
5. V_{IN (ON)}



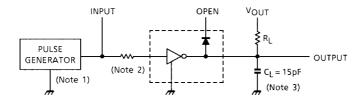
6. I_R

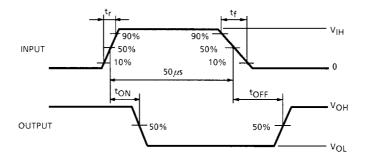


7. V_F



8. ton, toff





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50 Ω , $t_r \le 5$ ns, $t_f \le 10$ ns

Note 2: See below

INPUT CONDITION

TYPE NUMBER	R1	V _{IH}
TD62001P / AP / F / AF	2.7 kΩ	3 V
TD62002P / AP / F / AF	0	13 V
TD62003P / AP / F / AF	0	3 V
TD62004P / AP / F / AF	0	8 V

Note 3: C_L includes probe and jig capacitance.

PRECAUTIONS for USING

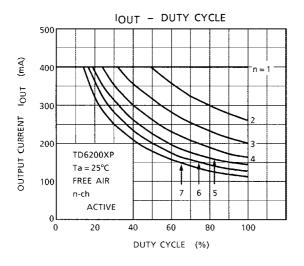
This IC does not include built-in protection circuits for excess current or overvoltage.

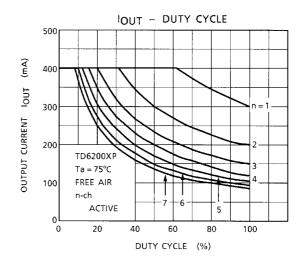
If this IC is subjected to excess current or overvoltage, it may be destroyed.

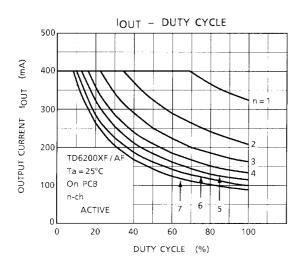
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

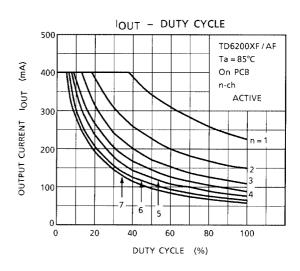
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

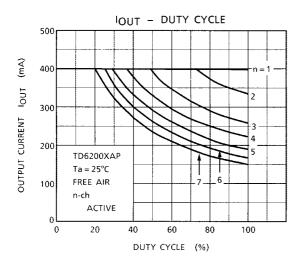
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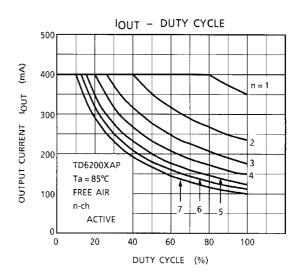


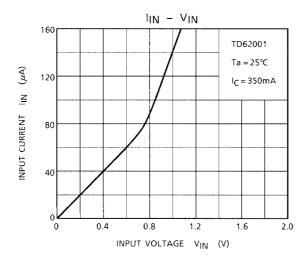


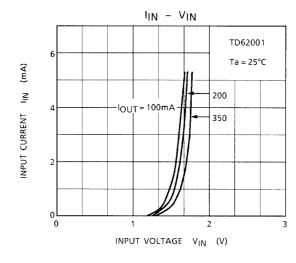


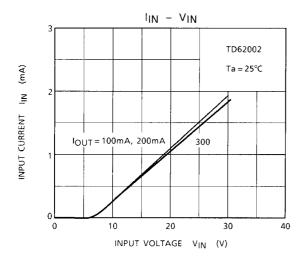


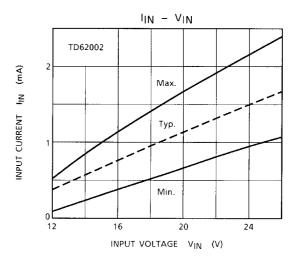


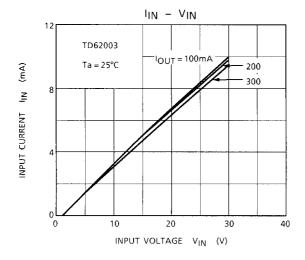


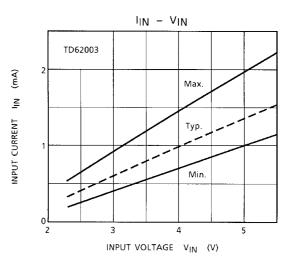


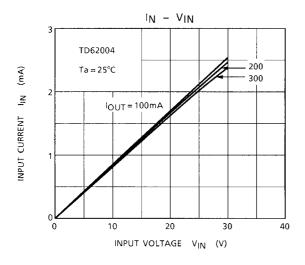


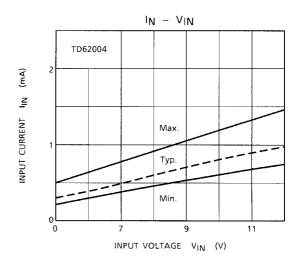


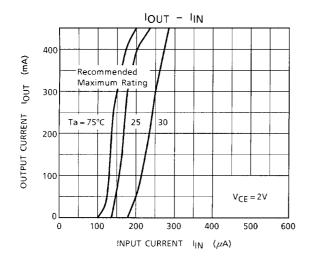


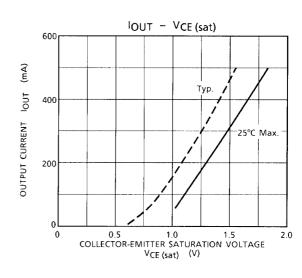


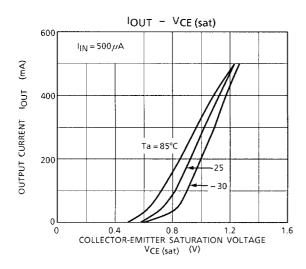


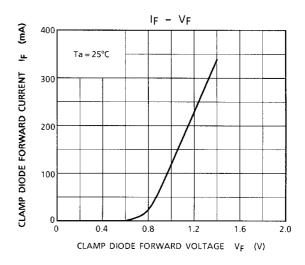


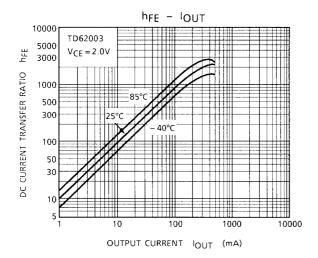


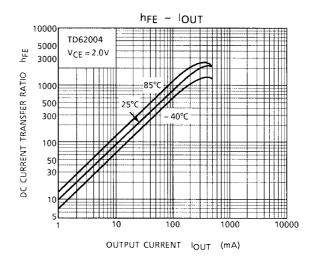


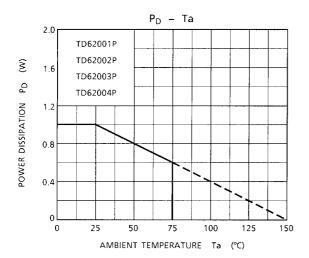


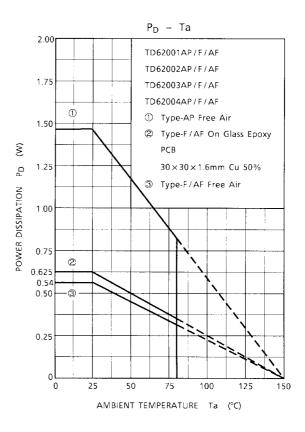






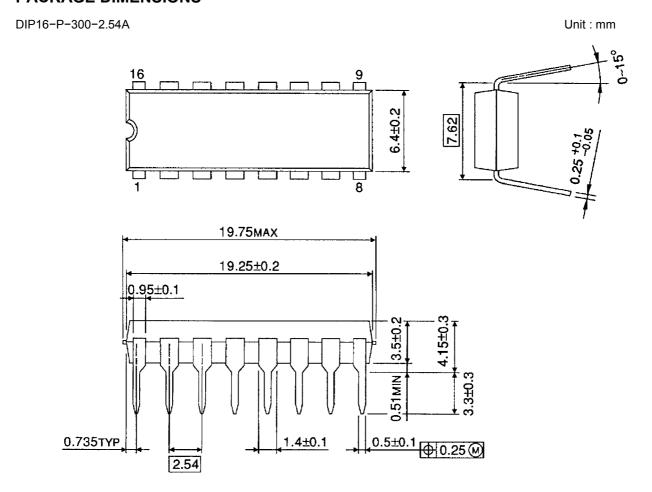






PACKAGE DIMENSIONS

TOSHIBA

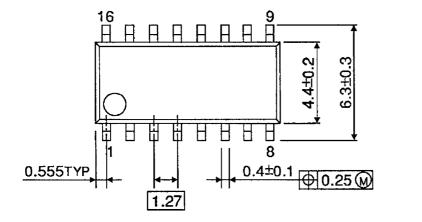


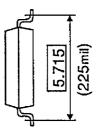
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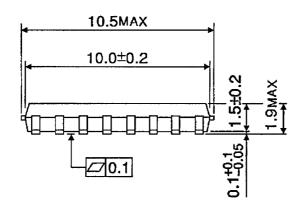
<u>TOSHIBA</u>

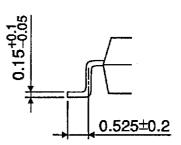
PACKAGE DIMENSIONS

SOP16-P-225-1.27 Unit: mm









Weight: 0.16 g (Typ.)

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RESTRICTIONS ON PRODUCT USE

000707EBA

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