TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

TD62107PG,TD62107FG

4ch High-current Darlington Sink Driver

The TD62107PG/FG are high-voltage, high-current darlington drivers and enable inputs which can gate the outputs. All units feature integral clamp diodes for switching inductive loads.

The TD62107PG/FG have a wide supply voltage range and all input are compatible with TTL and 5-V CMOS.

Application include relay, hammer, lamp and stepping moter drivers.

Please observe the thermal condition for using. The suffix (G) appended to the part number represents a RoHS-compatible product.

Features

- Output current (single output) 750 mA (max)
- High sustaining voltage output: 45 V min (TD62107PG)
 - 35 V min (TD62107FG)
- Output clamp diodes •
- Enable inputs E1, E2
- Wide supply voltage range $V_{CC} = 4.75$ to 7 V
- Input compatible with TTL and 5-V CMOS
- GND terminal = heat sink
- Package type-PG: DIP-16pin
- Package type-FG: HSOP-16pin

Pin Assignment (top-view)

TD62107PG





HSOP16-P-300-1.00

DIP16-R-300-2.54A

TD62107PG

TD62107FG



7062107FG

Schematics (each driver)



Truth Table

E1	Ē2	11 to 14	O1 to O4
L	L	L or H	Disable OFF
L	Н	L or H	Disable OFF
Н	L	LorH	Enable In
Н	Н	L or H	Disable OFF

Common

Output

In = I1 to I4

Output Equivalent Circuit

Input Equivalent Circuit



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Characteristics	Symbol	Rating	Unit		
Supply voltage	Vcc	-0.5 to 17	\supset_{V}		
Output sustaining		-0.5 to 45	V		
voltage	_∕VCE (SUS)	-0.5 to 35	v		
Output current	IOUT	750	mA		
Input voltage	V _{IN}	-0.5 to V _{CC} +	V		
Clamp diode reverse PG		45	V		
voltage	×R (35	V		
Clamp diode forword current	- 	500	mA		
Power discipation RG		2.7 (Note 1)	10/		
Fower dissipation		1.4 (Note 2)	vv		
Operating temperature	∼ T _{opr}	-40 to 85	°C		
Storage temperature	T _{stg}	-55 to 150	°C		

Note 1: On glass epoxy PCB ($50 \times 50 \times 1.6 \text{ mm}$ Cu 50%)

Note 2: On glass epoxy PCB ($60 \times 30 \times 1.6 \text{ mm Cu } 30\%$)

Operating Conditions (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition		Min	Тур.	Max	Unit
Supply voltage		V _{CC}	_		4.75	—	15	V
Output sustaining voltage	PG		_ <		0	—	45	V
	FG	VCE (SUS)			0	_	35	v
		IOUT	$T_{pw} = 25 \text{ ms}, \text{ Duty} = 75$	ns, Duty = 75%, 1 Circuit		1	500	
Output current	PG		T _{pw} = 25 ms, 4 Circuit	Duty = 30%		-7(400	mA
	FG			Duty = 40%	\sim	_	300	
Input voltage		V _{IN}	—	$\langle \langle $)0	_	V _{CC}	V
Clamp diode reverse voltage	PG	V _R	-			_	45	V
	FG					_	35	v
Clamp diode forward current		١ _F	—		_	(500	mA
Power dissipation	PG	Pa	_<		_	À	1.0	۱۸/
	FG	Ū	Ta = 85°C	(Note 1)		\sum	> 0.7	vV

Note1: On Glass Epoxy PCB ($60 \times 30 \times 1.6 \text{ mm Cu } 30\%$)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit		
	High level VIH Low level VIL		VIH <			2.0	_	V _{CC}	V	
input voltage			VIL					0.8	v	
Output current	High	PG	Іон	2	V _{CE} = 45 V, Ta = 75°C			100	μA	
	level	FG			V _{CE} = 35 V, Ta = 85°C	_		100		
Output voltage	Low lev		VOL	3	I _{OUT} = 50 mA			1.3	V	
		vei			$I_{OUT} = 750 \text{ mA}$	_		1.6		
Input current	High le	evel (// /ун	4	$V_{IN} = 13V$			100	μA	
	Lowle	vel		5 (VIN = 0.4 V	_	_	-0.3	mA	
Clamp diode reverse current FG				V _R =45 V	_	_	100	•		
		FG	'R	9	V _R = 35 V	_	_	100	μΑ	
Clamp diode forward voltage		VF	7	I _F = 500 mA	_	_	2.0	V		
Supply current	Output high	Output high	ICCH	4	V _{CC} = 13 V, V _{IN} = 0 V Output open			13	m۸	
	Output low	ICC	ST S	5	$V_{CC} = 13 \text{ V}, V_{IN} = 5 \text{ V}$ Output open	_		17	IIIA	
	PG	PG	ton	8	$V_{CC} = 5 \text{ V}, \text{ R}_{L} = 90 \Omega$ $C_{L} = 15 \text{ pF}, \text{ V}_{OUT} = 45 \text{ V}$		5		μs	
Number delay		FG			$V_{CC} = 5 \text{ V}, \text{ R}_{L} = 70 \Omega$ $C_{L} = 15 \text{ pF}, \text{ V}_{OUT} = 35 \text{ V}$		5			
Turn-off delay		PG	torr	Q		_	5	_	μs	
		FG	^L OFF	ŏ			5			

Test Circuit



Precautions for Using

This IC does not include built-in protection circuits for excess current or overvoltage.

If this IC is subjected to excess current or overvoltage, it may be destroyed.

Hence, the utmost care must be taken when systems which incorporate this IC are designed.

 $Utmost\ care\ is\ necessary\ in\ the\ design\ of\ the\ output\ line,\ V_{CC},\ COMMON\ and\ GND\ line\ since\ IC\ may\ be\ destroyed\ due\ to\ short-circuit\ between\ outputs,\ air\ contamination\ fault,\ or\ fault\ by\ improper\ grounding.$



Package Dimensions

DIP16-P-300-2.54A

Unit : mm



Package Dimensions



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