

# MOS FIELD EFFECT TRANSISTOR

2SK2462

## SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK2462 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

#### **FEATURES**

• Low On-Resistance

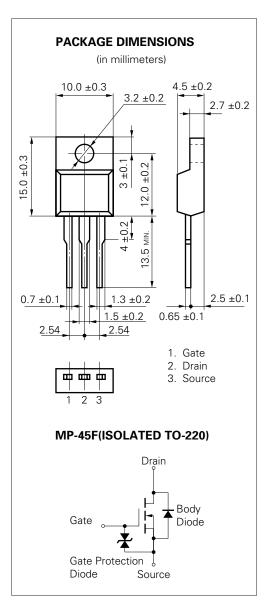
 $R_{DS(on)1} = 0.14 \ \Omega \ MAX.$  (@ Vgs = 10 V, ID = 8.0 A)  $R_{DS(on)2} = 0.17 \ \Omega \ MAX.$  (@ Vgs = 4 V, ID = 8.0 A)

- Low Ciss Ciss = 790 pF TYP.
- Built-in G-S Gate Protection Diodes
- High Avalanche Capability Ratings

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	VDSS	100	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	ID(DC)	±15	Α
Drain Current (pulse)*	I <sub>D(pulse)</sub>	±60	Α
Total Power Dissipation ( $T_c = 25$ °C)	P <sub>T1</sub>	30	W
Total Power Dissipation (TA = 25 $^{\circ}$ C)	P <sub>T2</sub>	2.0	W
Channel Temperature	$T_ch$	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C
Single Avalanche Current**	las	15	Α
Single Avalanche Energy**	Eas	22.5	mJ

- \* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %
- \*\* Starting Tch = 25 °C, Rg = 25  $\Omega$ , Vgs = 20 V  $\rightarrow$  0



The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding rated voltage may be applied to this device.

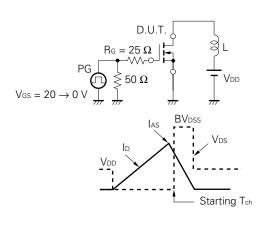


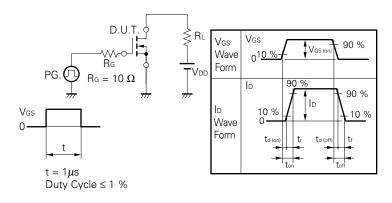
## **ELECTRICAL CHARACTERISTICS (TA = 25 °C)**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS(on)1		0.10	0.14	Ω	Vgs = 10 V, ID = 8.0 A
Drain to Source On-Resistance	R <sub>DS(on)2</sub>		0.12	0.17	Ω	Vgs = 4 V, ID = 8.0 A
Gate to Source Cutoff Voltage	V <sub>GS(off)</sub>	1.0	1.6	2.0	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	l y <sub>fs</sub> l	7.0	14		S	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 8.0 A
Drain Leakage Current	IDSS			10	μΑ	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0
Gate to Source Leakage Current	Igss			±10	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Input Capacitance	Ciss		790		pF	V <sub>DS</sub> = 10 V
Output Capacitance	Coss		280		pF	V <sub>G</sub> S = 0
Reverse Transfer Capacitance	Crss		88		pF	f = 1 MHz
Turn-On Delay Time	td(on)		16		ns	ID = 8.0 A
Rise Time	tr		110		ns	V <sub>GS(on)</sub> = 10 V
Turn-Off Delay Time	td(off)		88		ns	V <sub>DD</sub> = 50 V
Fall Time	tf		62		ns	$R_G = 10 \Omega$
Total Gate Charge	QG		33		nC	ID = 15 A
Gate to Source Charge	Qgs		5.4		nC	V <sub>DD</sub> = 80 V
Gate to Drain Charge	Q <sub>GD</sub>		25		nC	V <sub>GS</sub> = 10 V
Body Diode Forward Voltage	V <sub>F(S-D)</sub>		1.1		V	IF = 15 A, VGS = 0
Reverse Recovery Time	trr		160		ns	IF = 15 A, VGS = 0
Reverse Recovery Charge	Qrr		670		nC	di/dt = 100 A/μs

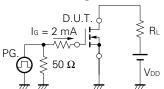
## **Test Circuit 1 Avalanche Capability**

### **Test Circuit 2 Switching Time**



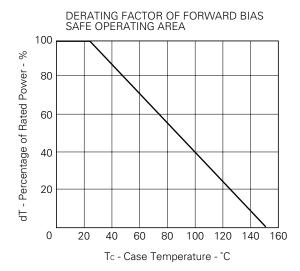


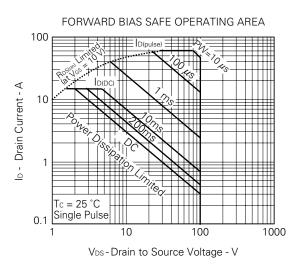
### **Test Circuit 3 Gate Charge**

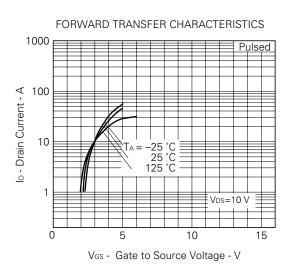


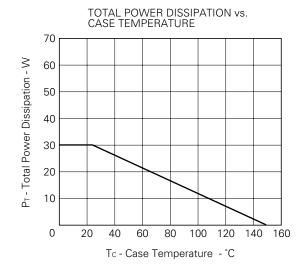
The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

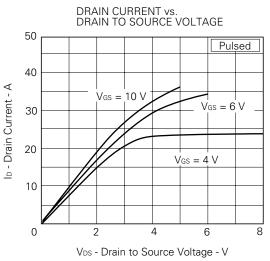
## TYPICAL CHARACTERISTICS (TA = 25 °C)





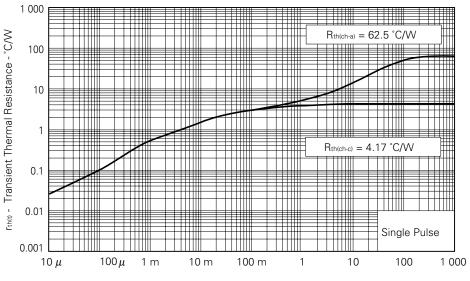






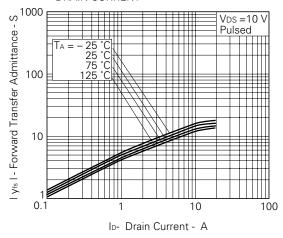


#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

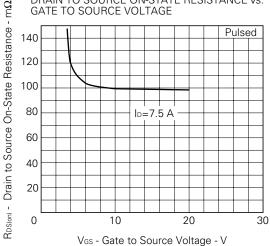


PW - Pulse Width - s

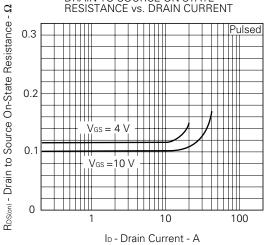
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



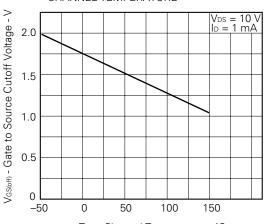
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

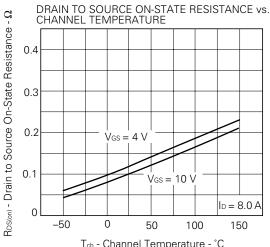


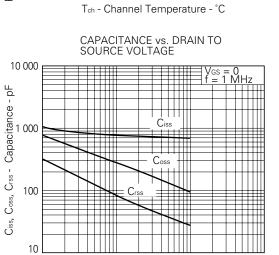
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

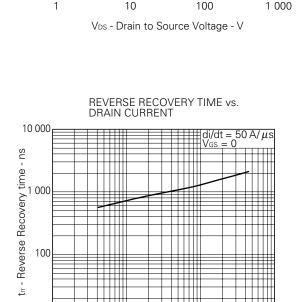


Tch - Channel Temperature - °C









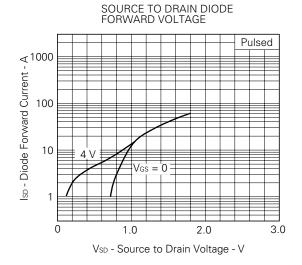
1.0

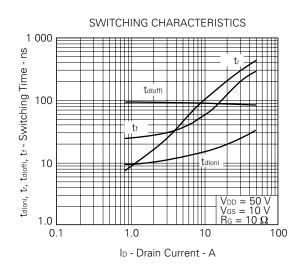
ID - Drain Current - A

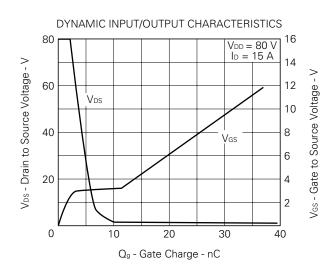
10

100

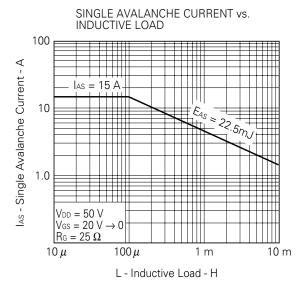
10\_\_

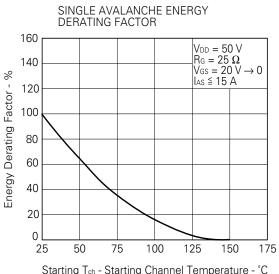














# REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

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